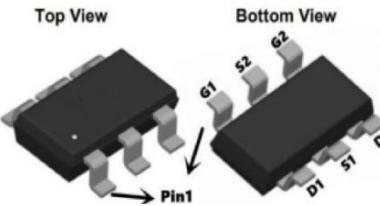


## Features

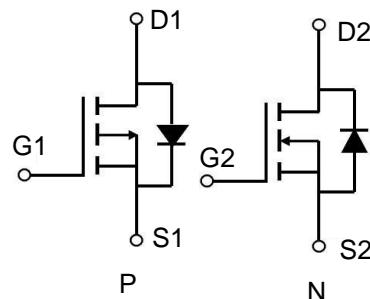
- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- Green Device Available

## Applications

- Power management in half bridge and inverters
- DC-DC Converter
- Load Switch



**SOT 23-6L**



## Absolute Maximum Ratings

Symbol	Parameter	Rating		Units
		N-Channel	P-Channel	
$V_{DS}$	Drain-Source Voltage	20	-20	V
$V_{GS}$	Gate-Source Voltage	$\pm 12$	$\pm 12$	V
$I_D @ T_c = 25^\circ C$	Continuous Drain Current, $V_{GS} @ 10V^1$	6.3	-4.1	A
$I_D @ T_c = 100^\circ C$	Continuous Drain Current, $V_{GS} @ 10V^1$	4.0	-2.6	A
$I_{DM}$	Pulsed Drain Current <sup>2</sup>	15	-16	A
EAS	Single Pulse Avalanche Energy <sup>3</sup>	72	59	mJ
$I_{AS}$	Avalanche Current	21	-19	A
$P_d @ T_c = 25^\circ C$	Total Power Dissipation <sup>4</sup>	1	1	W
$T_{STG}$	Storage Temperature Range	-55 to 150	-55 to 150	°C
$T_J$	Operating Junction Temperature Range	-55 to 150	-55 to 150	°C

## Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-Ambient <sup>1</sup>	---	125	°C/W
$R_{\theta JC}$	Thermal Resistance Junction-Case <sup>1</sup>	---	70	°C/W

**Electrical Characteristics ( $T_J=25^\circ\text{C}$ , unless otherwise noted)**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$ , $I_D=250\mu\text{A}$	20	---	---	V
$\text{R}_{\text{DS(ON)}}$	Static Drain-Source On-Resistance <sup>2</sup>	$V_{\text{GS}}=4.5\text{V}$ , $I_D=3\text{A}$	---	17	25	$\text{m}\Omega$
		$V_{\text{GS}}=2.5\text{V}$ , $I_D=2\text{A}$	---	21	30	
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$ , $I_D=250\mu\text{A}$	0.4	---	1.2	V
$\text{I}_{\text{DSS}}$	Drain-Source Leakage Current	$V_{\text{DS}}=16\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $T_J=25^\circ\text{C}$	---	---	1	$\text{uA}$
		$V_{\text{DS}}=16\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $T_J=55^\circ\text{C}$	---	---	5	
$\text{I}_{\text{GSS}}$	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 12\text{V}$ , $V_{\text{DS}}=0\text{V}$	---	---	$\pm 100$	nA
$\text{g}_{\text{fs}}$	Forward Transconductance	$V_{\text{DS}}=5\text{V}$ , $I_D=3\text{A}$	---	10.5	---	S
$\text{Q}_g$	Total Gate Charge (4.5V)	$V_{\text{DS}}=15\text{V}$ , $V_{\text{GS}}=4.5\text{V}$ , $I_D=3\text{A}$	---	4.6	---	$\text{nC}$
$\text{Q}_{\text{gs}}$	Gate-Source Charge		---	0.7	---	
$\text{Q}_{\text{gd}}$	Gate-Drain Charge		---	1.5	---	
$\text{T}_{\text{d(on)}}$	Turn-On Delay Time	$V_{\text{DD}}=10\text{V}$ , $V_{\text{GS}}=4.5\text{V}$ , $R_G=3.3\Omega$	---	1.6	---	$\text{ns}$
$\text{T}_r$	Rise Time		---	42	---	
$\text{T}_{\text{d(off)}}$	Turn-Off Delay Time		---	14	---	
$\text{T}_f$	Fall Time		---	7	---	
$\text{C}_{\text{iss}}$	Input Capacitance	$V_{\text{DS}}=15\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $f=1\text{MHz}$	---	310	---	$\text{pF}$
$\text{C}_{\text{oss}}$	Output Capacitance		---	49	---	
$\text{C}_{\text{rss}}$	Reverse Transfer Capacitance		---	35	---	

**Diode Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$\text{I}_s$	Continuous Source Current <sup>1,4</sup>	$V_G=V_D=0\text{V}$ , Force Current	---	---	6.0	A
$\text{V}_{\text{SD}}$	Diode Forward Voltage <sup>2</sup>	$V_{\text{GS}}=0\text{V}$ , $\text{I}_s=1\text{A}$ , $T_J=25^\circ\text{C}$	---	---	1.2	V

Note :

- 1.The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width  $\leq 300\text{us}$  , duty cycle  $\leq 2\%$
- 3.The power dissipation is limited by  $150^\circ\text{C}$  junction temperature
- 4.The data is theoretically the same as  $I_D$  and  $I_{DM}$  , in real applications , should be limited by total power dissipation.

**Electrical Characteristics ( $T_J=25^\circ C$ , unless otherwise noted)**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=-250\mu A$	-20	---	---	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance <sup>2</sup>	$V_{GS}=-4.5V, I_D=-4.1A$	---	30	38	$m\Omega$
		$V_{GS}=-2.5V, I_D=-3A$	---	38	53	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}, I_D=-250\mu A$	-0.4	-0.7	-1	V
$I_{DSS}$	Drain-Source Leakage Current	$V_{DS}=-20V, V_{GS}=0V, T_J=25^\circ C$	---	---	1	$\mu A$
		$V_{DS}=-20V, V_{GS}=0V, T_J=55^\circ C$	---	---	5	
$I_{GSS}$	Gate-Source Leakage Current	$V_{GS}=\pm 12V, V_{DS}=0V$	---	---	$\pm 100$	nA
$g_{fs}$	Forward Transconductance	$V_{DS}=5V, I_D=3A$	---	---	---	S
$Q_g$	Total Gate Charge (4.5V)	$V_{DS}=-10V, V_{GS}=-4.5V, I_D=-2A$	---	8.8	---	nC
$Q_{gs}$	Gate-Source Charge		---	1.4	---	
$Q_{gd}$	Gate-Drain Charge		---	1.9	---	
$T_{d(on)}$	Turn-On Delay Time	$V_{DD}=-10V, V_{GS}=-4.5V, R_G=1\Omega$	---	10	---	ns
$T_r$	Rise Time		---	32	---	
$T_{d(off)}$	Turn-Off Delay Time		---	50	---	
$T_f$	Fall Time		---	51	---	
$C_{iss}$	Input Capacitance	$V_{DS}=-10V, V_{GS}=0V, f=1MHz$	---	830	---	pF
$C_{oss}$	Output Capacitance		---	132	---	
$C_{rss}$	Reverse Transfer Capacitance		---	85	---	

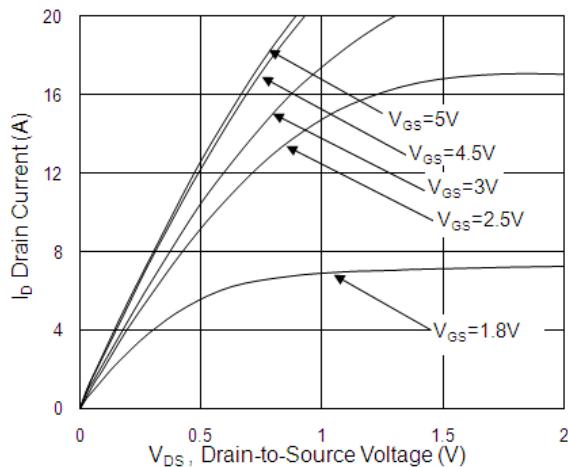
**Diode Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$I_s$	Continuous Source Current <sup>1,4</sup>	$V_G=V_D=0V$ , Force Current	---	---	-4.1	A
$V_{SD}$	Diode Forward Voltage <sup>2</sup>	$V_{GS}=0V, I_s=-4.1A, T_J=25^\circ C$	---	---	-1.2	V

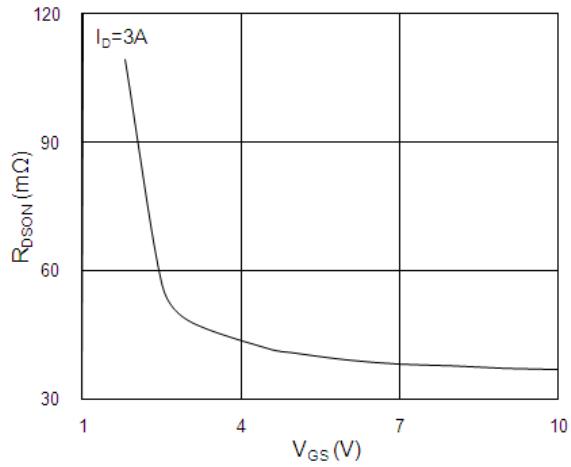
Note :

- 1.The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width  $\leq 300\mu s$  , duty cycle  $\leq 2\%$
- 3.The power dissipation is limited by  $150^\circ C$  junction temperature
- 4.The data is theoretically the same as  $I_D$  and  $I_{DM}$  , in real applications , should be limited by total power dissipation.

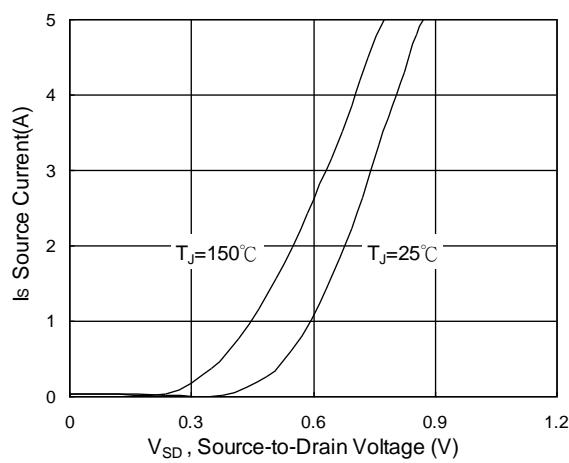
### N-Channel Typical Characteristics



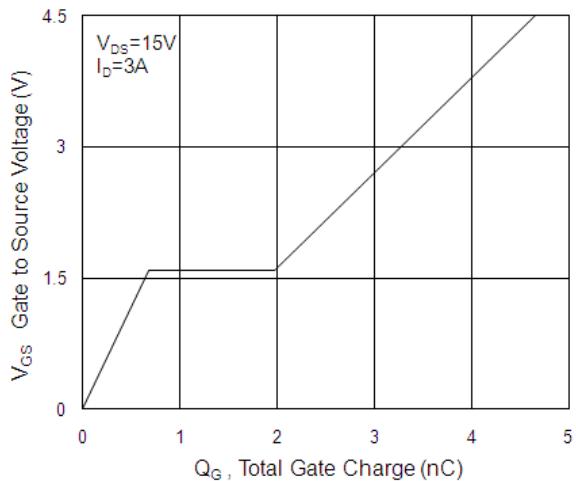
**Fig.1 Typical Output Characteristics**



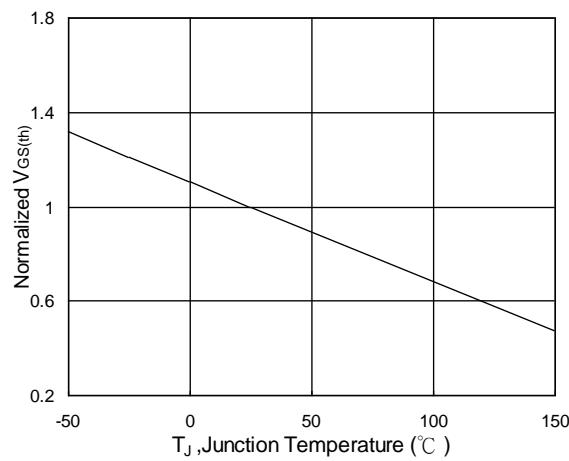
**Fig.2 On-Resistance vs. Gate-Source Voltage**



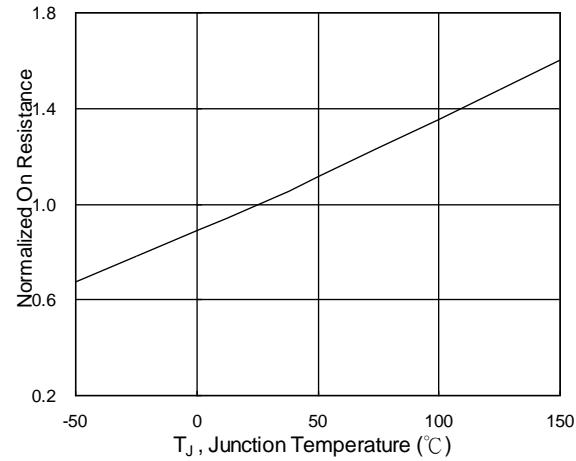
**Fig.3 Forward Characteristics of Reverse**



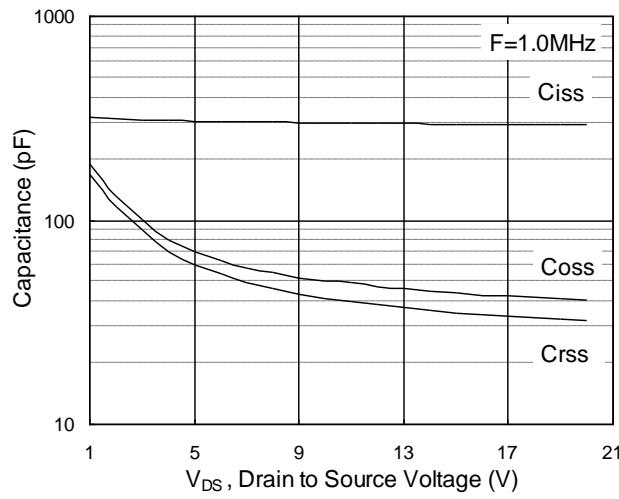
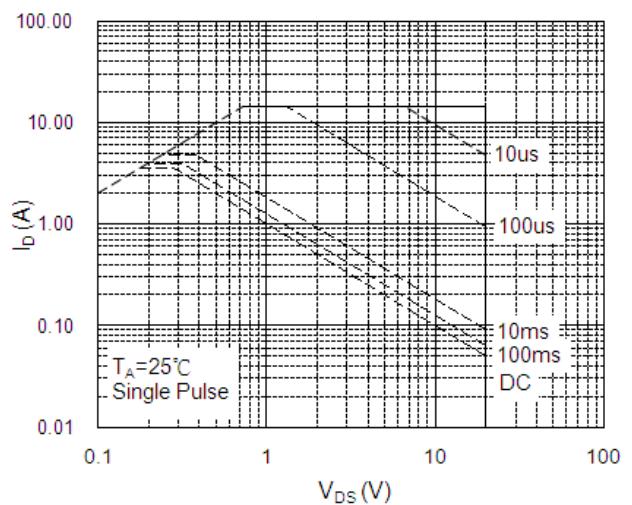
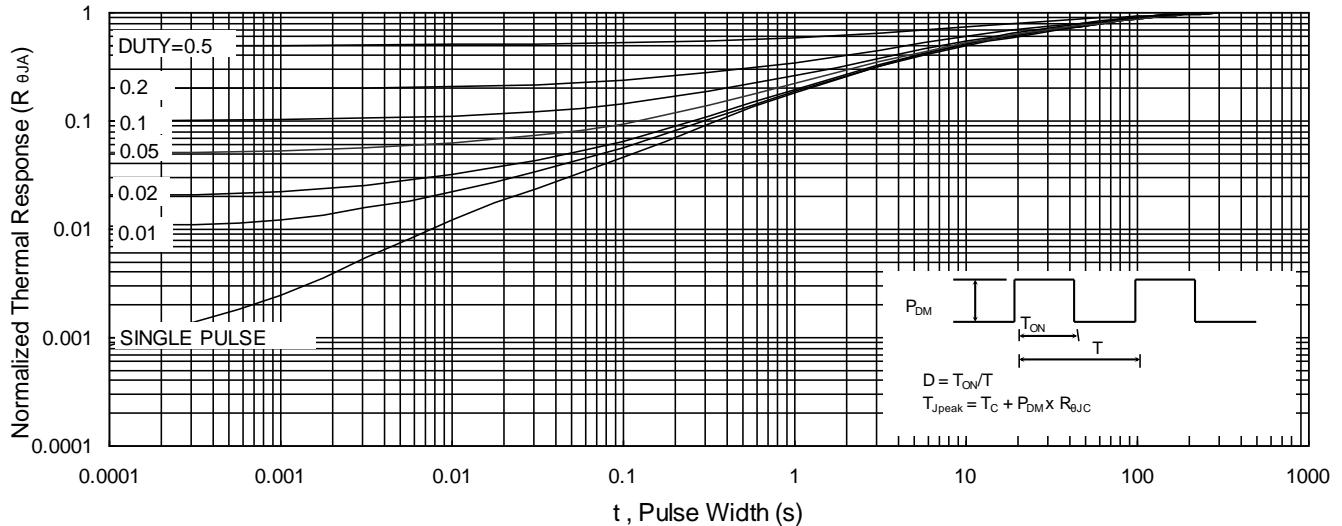
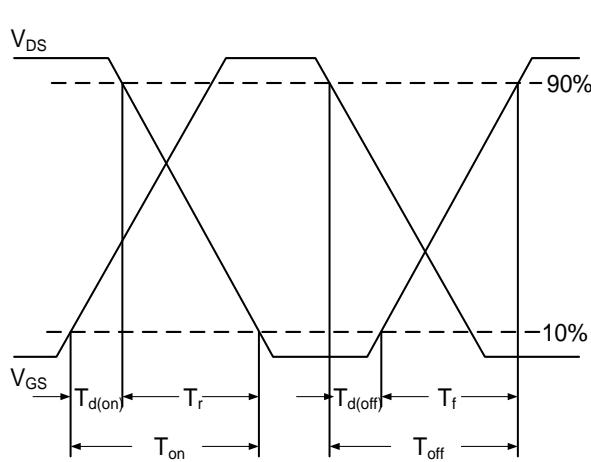
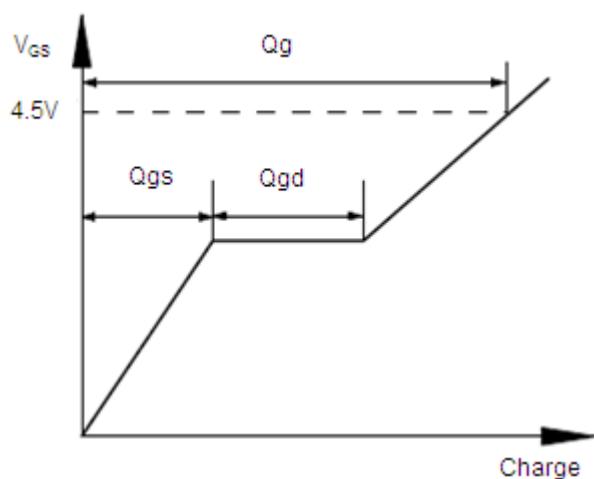
**Fig.4 Gate-Charge Characteristics**



**Fig.5 Normalized  $V_{GS(th)}$  vs.  $T_J$**

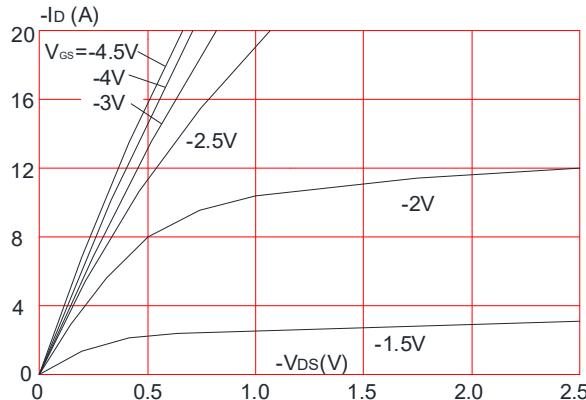


**Fig.6 Normalized  $R_{DS(on)}$  vs.  $T_J$**

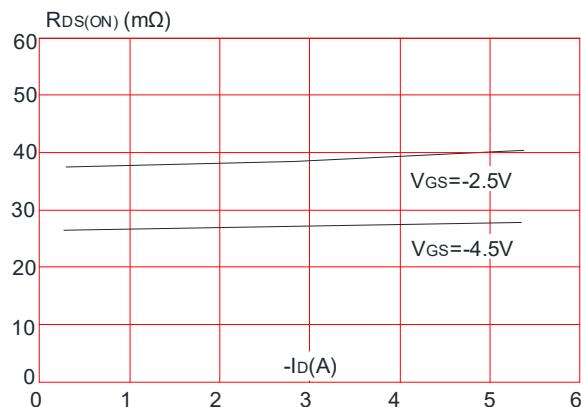

**Fig.7 Capacitance**

**Fig.8 Safe Operating Area**

**Fig.9 Normalized Maximum Transient Thermal Impedance**

**Fig.10 Switching Time Waveform**

**Fig.11 Gate Charge Waveform**

### P-Channel Typical Characteristics

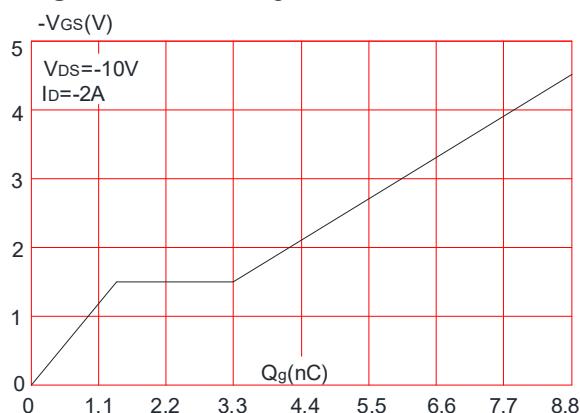
**Figure 1:** Output Characteristics



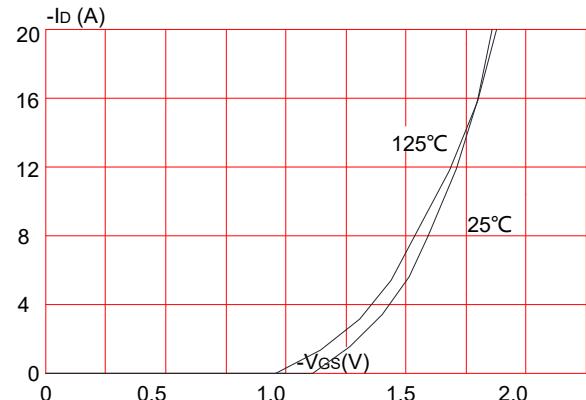
**Figure 3:** On-resistance vs. Drain Current



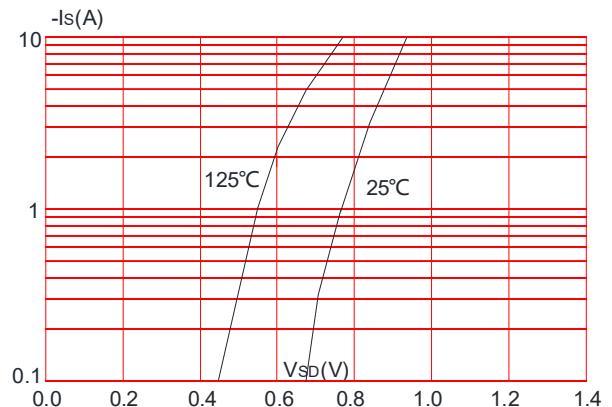
**Figure 5:** Gate Charge Characteristics



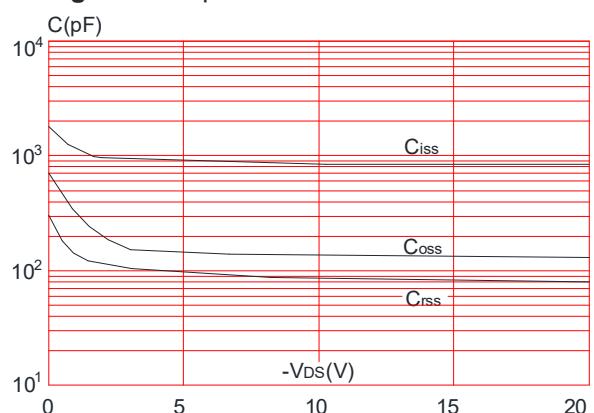
**Figure 2:** Typical Transfer Characteristics



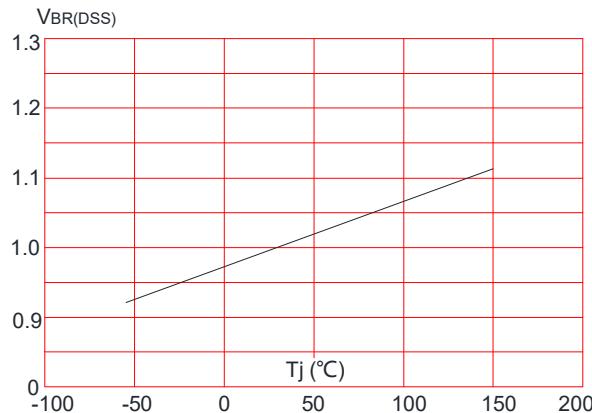
**Figure 4:** Body Diode Characteristics



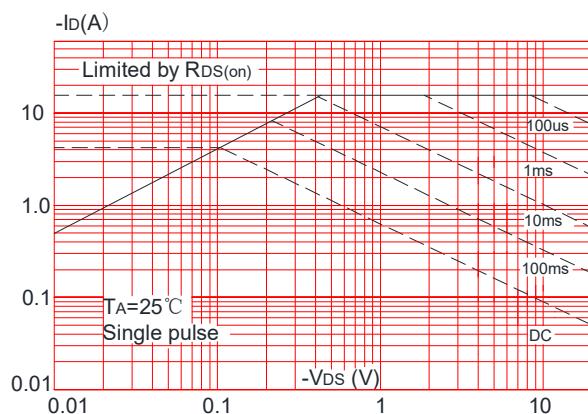
**Figure 6:** Capacitance Characteristics



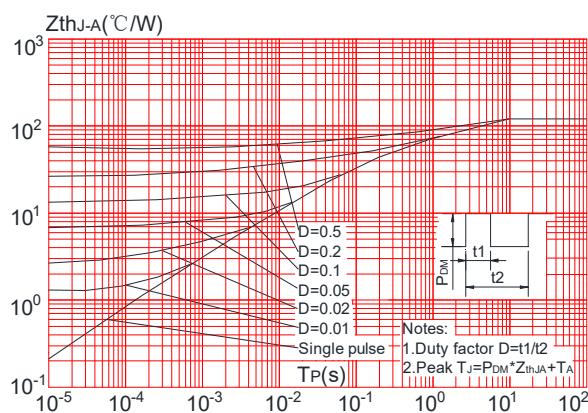
**Figure 7:** Normalized Breakdown Voltage vs. Junction Temperature



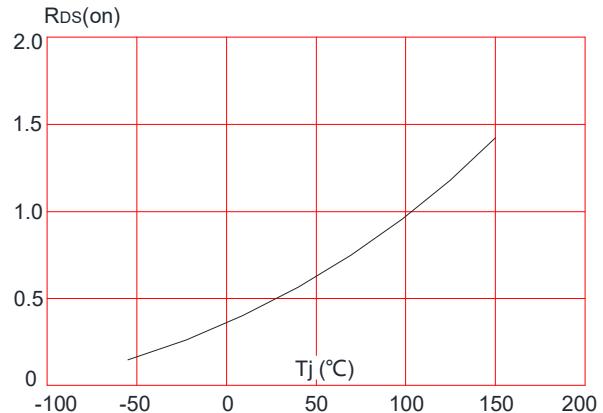
**Figure 9:** Maximum Safe Operating Area



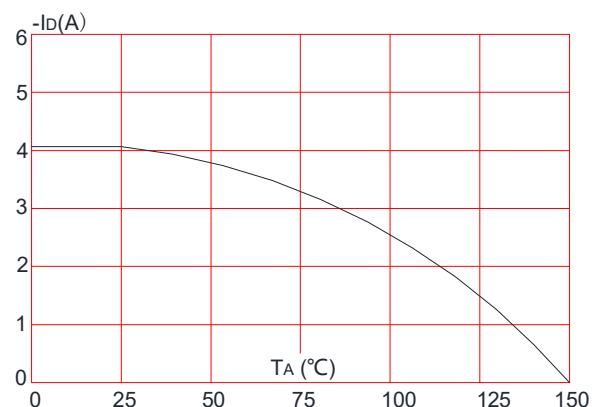
**Figure 11:** Maximum Effective Transient Thermal Impedance, Junction-to-Ambient



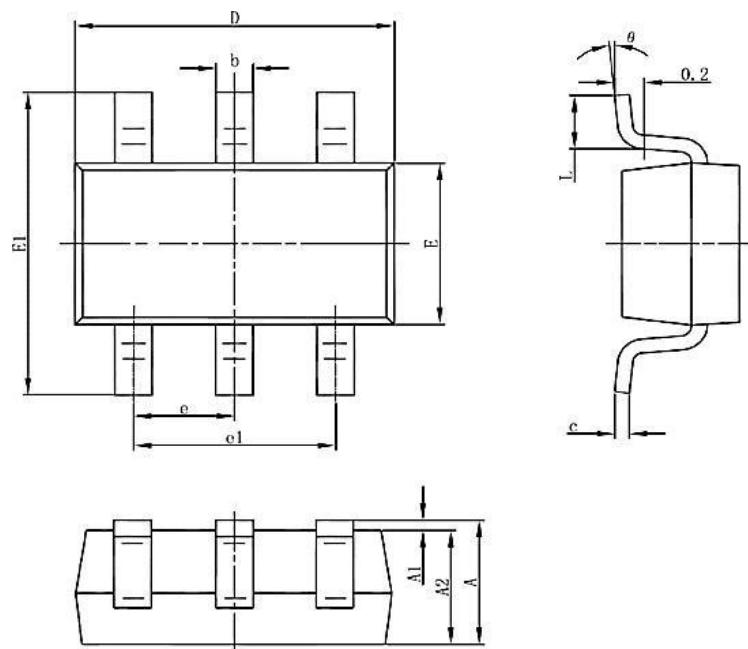
**Figure 8:** Normalized on Resistance vs. Junction Temperature



**Figure 10:** Maximum Continuous Drain Current vs. Ambient Temperature



## Package Mechanical Data-SOT23-6-Double



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
C	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E	1.500	1.700	0.059	0.067
E1	2.650	2.950	0.104	0.116
e	0.950 (BSC)		0.037(BSC)	
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
θ	0	8	0	8