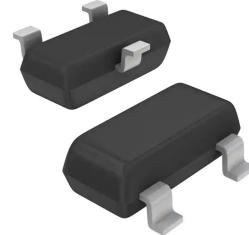


Features

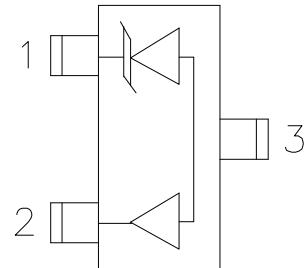
- For Sensitive ESD Protection
- Two Devices Protect One Line
- Low Capacitance for High-Speed Data Line
- Low Clamping Voltage
- Moisture Sensitivity Level 1
- Epoxy Meets UL 94 V-0 Flammability Rating



SOT-23

Maximum Ratings

- Operating Junction Temperature Range: -55°C to +125°C
- Storage Temperature Range: -55°C to +150°C



IEC61000-4-2(ESD)	Air Contact	±30KV ±30KV
Peak Pulse Current(8/20μs)	I _{PP}	12A
Peak Pulse Power (8/20us)	P _{PK}	300W

Electrical Characteristics @ 25°C (Unless Otherwise Specified)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Units
Reverse Working Voltage	V _{RWM}				12	V
Reverse Breakdown Voltage	V _{BR}	I _T = 1mA	13.3			V
Reverse Leakage Current	I _R	V _{RWM} = 12V			0.5	μA
Clamping Voltage	V _C	I _{PP} =1A, t _P =8/20μs			19	V
Clamping Voltage	V _C	I _{PP} =5A, t _P =8/20μs			24	V
Junction Capacitance	C _J	V _R = 0V, f = 1MHz, Pin 1 to Pin 2			5	pF

Curve Characteristics

Fig. 1 - 8 X 20 μ s Pulse Waveform

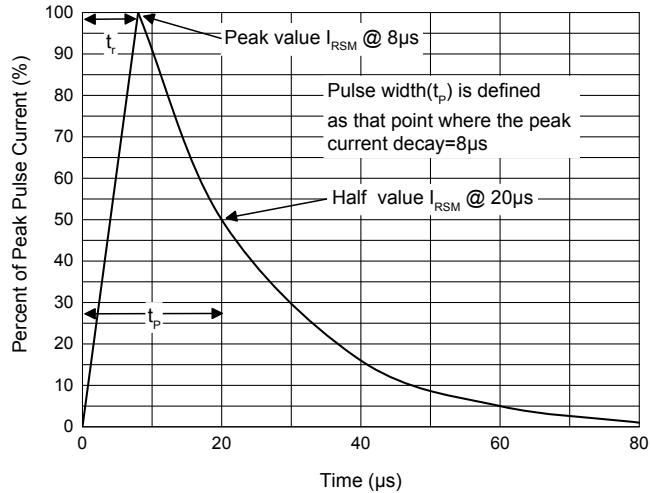
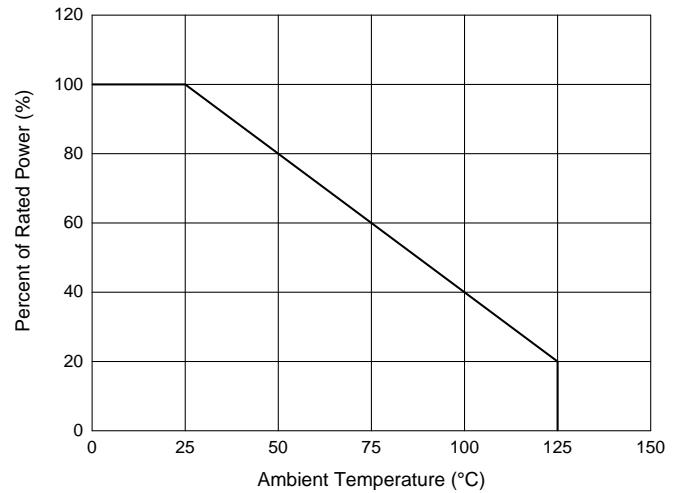
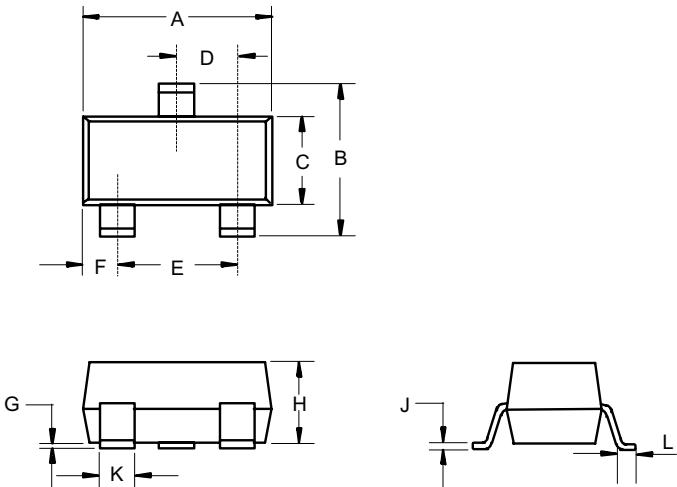


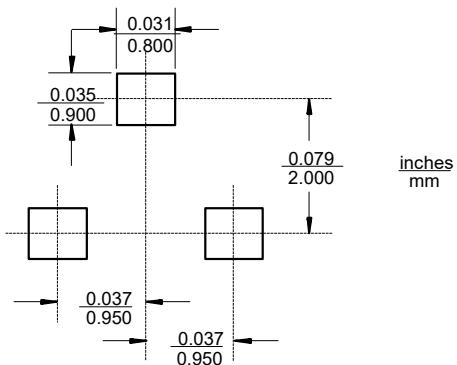
Fig. 2 - Pulse Derating Curve





DIM	INCHES		MM		NOTE
	MIN	MAX	MIN	MAX	
A	0.110	0.120	2.80	3.04	
B	0.083	0.104	2.10	2.64	
C	0.047	0.055	1.20	1.40	
D	0.034	0.041	0.85	1.05	
E	0.067	0.083	1.70	2.10	
F	0.018	0.024	0.45	0.60	
G	0.0004	0.006	0.01	0.15	
H	0.035	0.043	0.90	1.10	
J	0.003	0.007	0.08	0.18	
K	0.012	0.020	0.30	0.51	
L	0.007	0.020	0.20	0.50	

Suggested Solder Pad Layout



CCS Semiconductor and **eSEMI** are trademarks of Semiconductor Components Industries, CCS Semiconductor reserves the right to make changes without further notice to any products herein. CCS Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does CCS Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. CCS Semiconductor does not convey any license under its patent rights nor the rights of others.