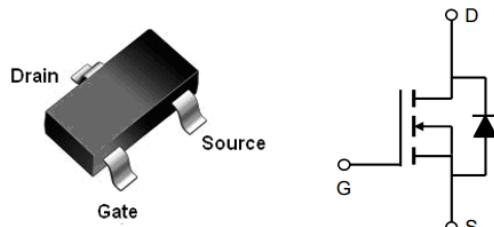


Features

- Low $R_{DS(on)}$ @ $V_{GS}=4.5V$
- 3.3V Logic Level Control
- N Channel SOT23-3L Package
- Pb-Free, RoHS Compliant

Applications

- Load Switch
- DC/DC Converter
- Switching Circuits
- LED Driver


SOT23-3L

Absolute Maximum Ratings

Symbol	Parameter	Rating	Unit
Common Ratings (TA=25°C Unless Otherwise Noted)			
V_{GS}	Gate-Source Voltage	± 12	V
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	20	V
T_J	Maximum Junction Temperature	150	°C
T_{STG}	Storage Temperature Range	-50 to 150	°C
Mounted on Large Heat Sink			
I_{DM}	Pulse Drain Current Tested①	$T_A = 25^\circ C$	18
I_D	Continuous Drain Current($V_{GS}=4.5V$)	$T_A = 25^\circ C$	3.6
		$T_A = 70^\circ C$	2.8
P_D	Maximum Power Dissipation	$T_A = 25^\circ C$	1
		$T_A = 70^\circ C$	0.8
$R_{\theta JA}$	Thermal Resistance Junction-Ambient	100	°C/W

Static Electrical Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise stated)

Symbol	Parameter	Condition	Min	Typ	Max	Unit
$V_{(\text{BR})\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_D=250\mu\text{A}$	20	--	--	V
I_{DSS}	Zero Gate Voltage Drain Current($T_A=25^\circ\text{C}$)	$V_{\text{DS}}=20\text{V}$, $V_{\text{GS}}=0\text{V}$	--	--	1	μA
	Zero Gate Voltage Drain Current($T_A=125^\circ\text{C}$)	$V_{\text{DS}}=16\text{V}$, $V_{\text{GS}}=0\text{V}$	--	--	100	μA
I_{GSS}	Gate-Body Leakage Current	$V_{\text{GS}}=\pm 12\text{V}$, $V_{\text{DS}}=0\text{V}$	--	--	± 100	nA
$V_{\text{GS}(\text{TH})}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}$, $I_D=250\mu\text{A}$	0.4	0.6	1.0	V
$R_{\text{DS}(\text{ON})}$	Drain-Source On-State Resistance②	$V_{\text{GS}}=4.5\text{V}$, $I_D=3\text{A}$	--	28	35	$\text{m}\Omega$
$R_{\text{DS}(\text{ON})}$	Drain-Source On-State Resistance②	$V_{\text{GS}}=3.3\text{V}$, $I_D=2\text{A}$	--	32	42	$\text{m}\Omega$
$R_{\text{DS}(\text{ON})}$	Drain-Source On-State Resistance②	$V_{\text{GS}}=2.5\text{V}$, $I_D=1\text{A}$	--	36	48	$\text{m}\Omega$

Dynamic Electrical Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise stated)

C_{iss}	Input Capacitance	$V_{\text{DS}}=10\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	--	280	--	pF
C_{oss}	Output Capacitance		--	46	--	pF
C_{rss}	Reverse Transfer Capacitance		--	42	--	pF
Q_g	Total Gate Charge	$V_{\text{DS}}=10\text{V}$, $I_D=3\text{A}$, $V_{\text{GS}}=5\text{V}$	--	4.7	--	nC
Q_{gs}	Gate Source Charge		--	0.6	--	nC
Q_{gd}	Gate Drain Charge		--	1.7	--	nC

Switching Characteristics

$t_{\text{d(on)}}$	Turn on Delay Time	$V_{\text{DD}}=10\text{V}$, $I_D=4\text{A}$, $R_G=3.3\Omega$, $V_{\text{GS}}=4.5\text{V}$	--	11	--	ns
t_r	Turn on Rise Time		--	35	--	ns
$t_{\text{d(off)}}$	Turn Off Delay Time		-	25	--	ns
t_f	Turn Off Fall Time		--	32	--	ns

Source Drain Diode Characteristics

I_{SD}	Source drain current(Body Diode)	$T_A=25^\circ\text{C}$	--	--	1.8	A
V_{SD}	Forward on voltage②	$T_j=25^\circ\text{C}$, $I_{\text{SD}}=2\text{A}$, $V_{\text{GS}}=0\text{V}$	--	0.74	1.2	V

Notes:

① Pulse width limited by maximum allowable junction temperature

②Pulse test ; Pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$.

Ver.1.0

Typical Characteristics

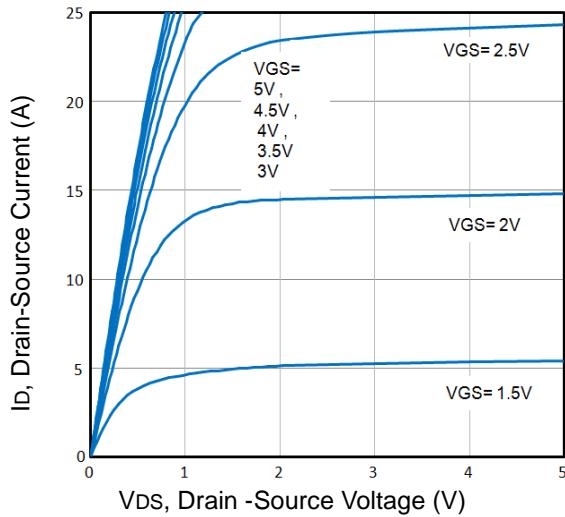


Fig1. Typical Output Characteristics

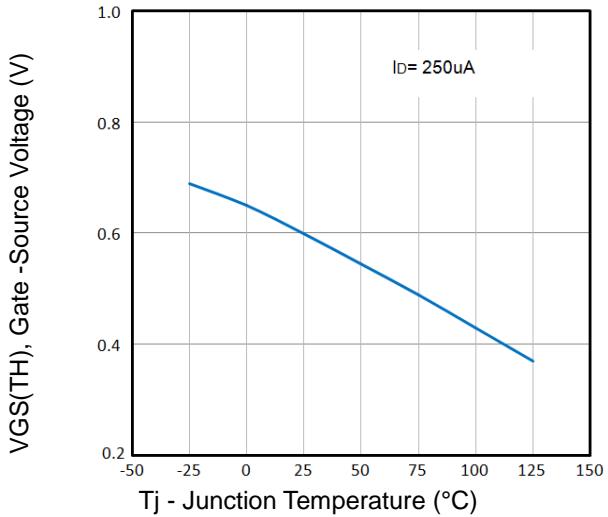


Fig2. Normalized Threshold Voltage Vs. Temperature

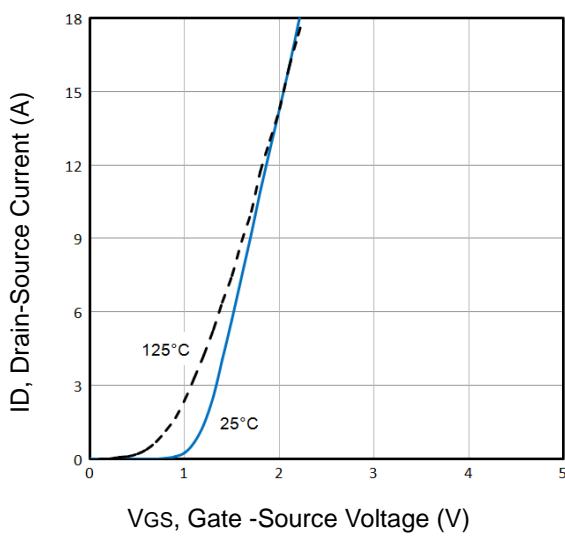


Fig3. Typical Transfer Characteristics

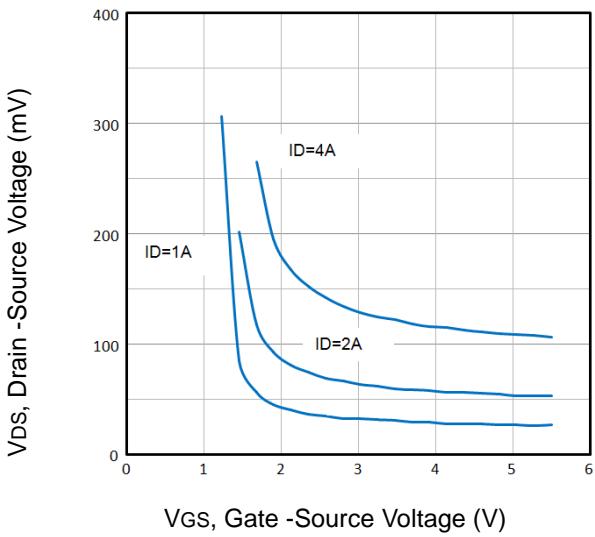


Fig4. Drain -Source Voltage vs Gate -Source Voltage

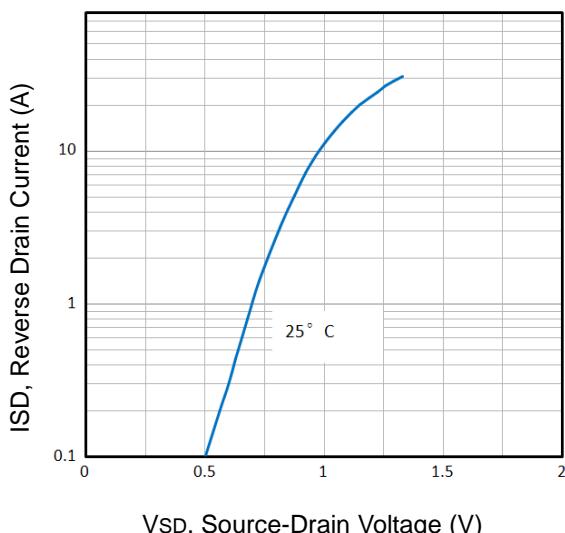


Fig5. Typical Source-Drain Diode Forward Voltage

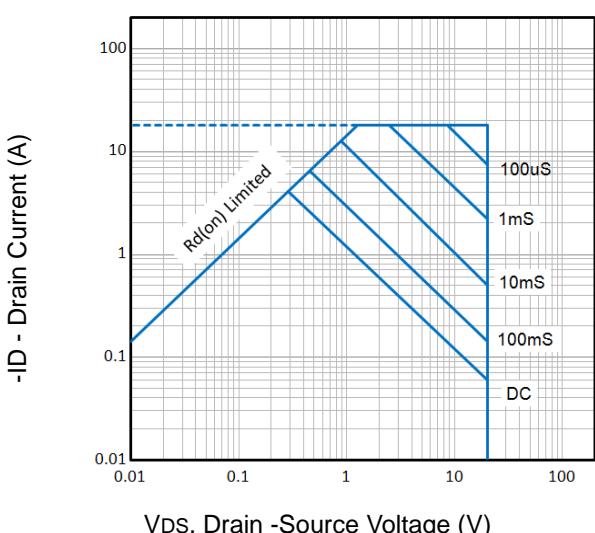


Fig6. Maximum Safe Operating Area

Typical Characteristics

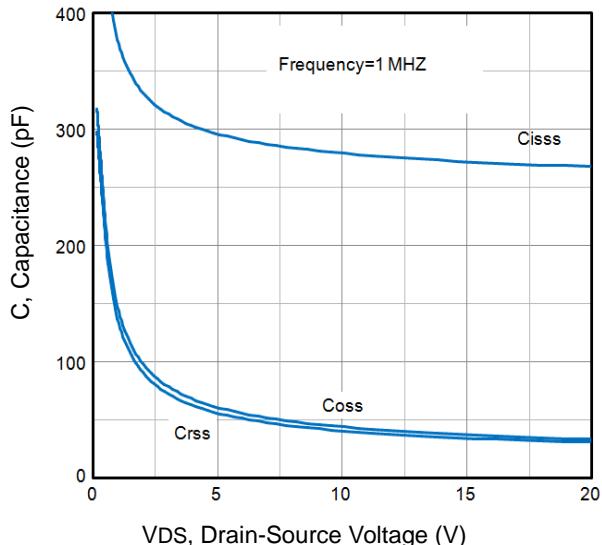


Fig7. Typical Capacitance Vs. Drain-Source Voltage

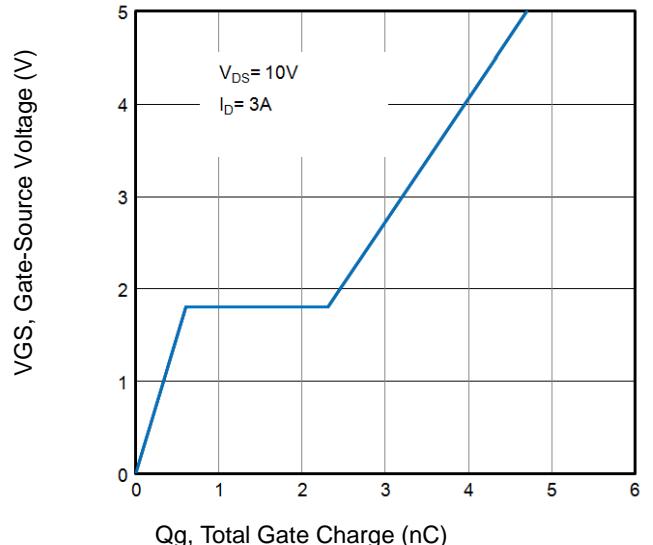


Fig8. Typical Gate Charge Vs. Gate-Source Voltage

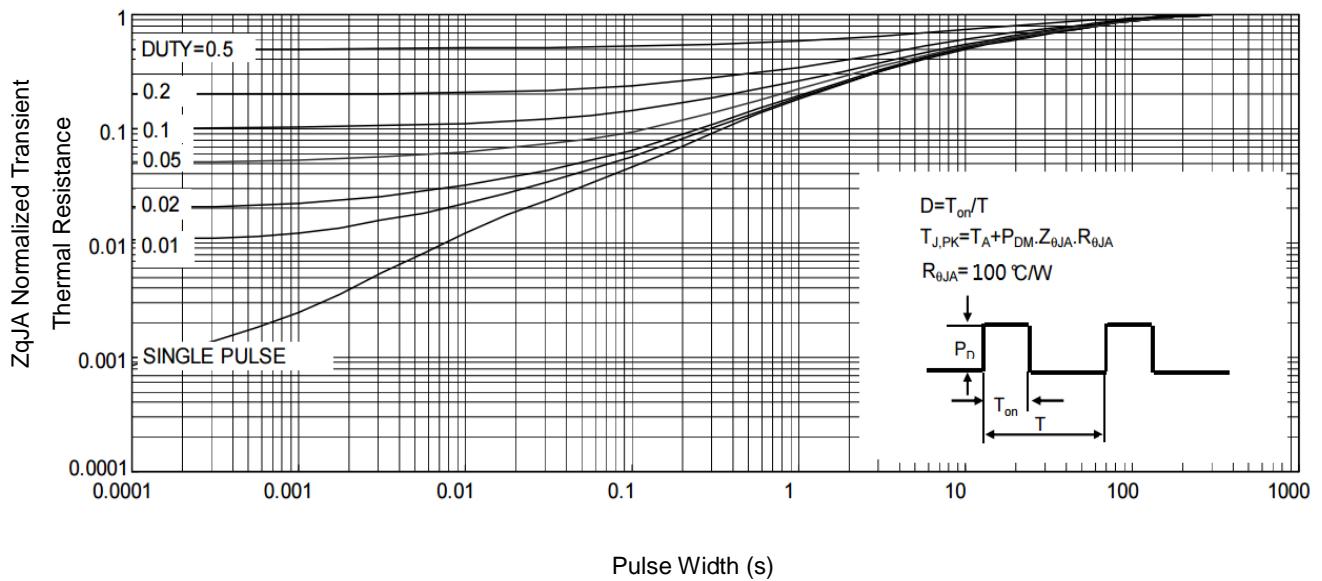


Fig9. Normalized Maximum Transient Thermal Impedance

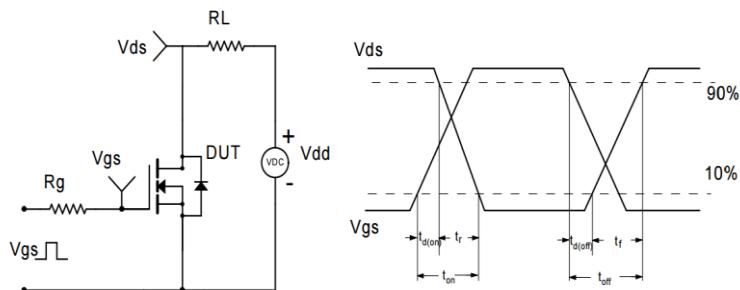


Fig10. Switching Time Test Circuit and waveforms