

CSM320N2S323

30V N-Channel Enhancement Mode MOSFET

Voltage

30 V

Current

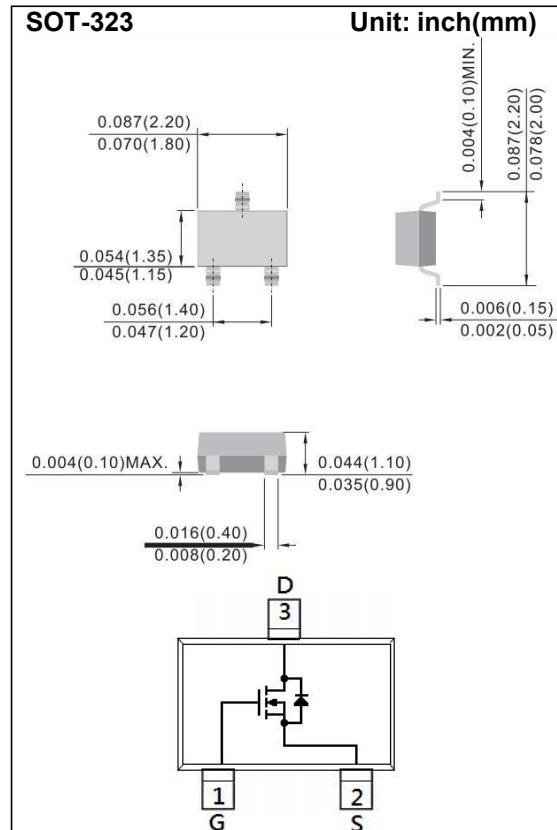
2A

Features

- R_{DS(ON)}, V_{GS}@10V, I_D@1.9A<70mΩ
- R_{DS(ON)}, V_{GS}@4.5V, I_D@1.6A<75mΩ
- R_{DS(ON)}, V_{GS}@2.5V, I_D@1.2A<85mΩ
- R_{DS(ON)}, V_{GS}@1.8V, I_D@0.7A<110mΩ
- Advanced Trench Process Technology

Mechanical Data

- Case: SOT-323 Package
- Terminals: Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 0.00018 ounces, 0.005 grams



Maximum Ratings and Thermal Characteristics ($T_A=25^\circ\text{C}$ unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNITS
Drain-Source Voltage	V _{DS}	30	V
Gate-Source Voltage	V _{GS}	+20	V
Continuous Drain Current	I _D	2	A
Pulsed Drain Current	I _{DM}	7	A
Power Dissipation	T _a =25°C	350	mW
	Derate above 25°C	2.8	mW/ °C
Operating Junction and Storage Temperature Range	T _J , T _{STG}	-55~150	°C
Typical Thermal resistance - Junction to Ambient (Note 3)	R _{θJA}	357	°C/W

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Electrical Characteristics ($T_A=25^\circ\text{C}$ unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$	30	-	-	V
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250\mu\text{A}$	0.4	0.72	1.2	V
Drain-Source On-State Resistance	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=1.9\text{A}$	-	58	70	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}, I_{\text{D}}=1.6\text{A}$	-	61	75	
		$V_{\text{GS}}=2.5\text{V}, I_{\text{D}}=1.2\text{A}$	-	69	85	
		$V_{\text{GS}}=1.8\text{V}, I_{\text{D}}=0.7\text{A}$	-	80	110	
Zero Gate Voltage Drain Current	I_{DSS}	$V_{\text{DS}}=30\text{V}, V_{\text{GS}}=0\text{V}$	-	0.01	1	μA
Gate-Source Leakage Current	I_{GSS}	$V_{\text{GS}}=\pm 12\text{V}, V_{\text{DS}}=0\text{V}$	-	± 10	± 100	nA
Dynamic						
Total Gate Charge	Q_g	$V_{\text{DS}}=15\text{V}, I_{\text{D}}=1.9\text{A}, V_{\text{GS}}=10\text{V}$ <small>(Note 1,2)</small>	-	4.8	-	nC
Gate-Source Charge	Q_{gs}		-	0.5	-	
Gate-Drain Charge	Q_{gd}		-	0.7	-	
Input Capacitance	C_{iss}	$V_{\text{DS}}=15\text{V}, V_{\text{GS}}=0\text{V}, f=1.0\text{MHz}$	-	447	-	pF
Output Capacitance	C_{oss}		-	34	-	
Reverse Transfer Capacitance	C_{rss}		-	22	-	
Switching						
Turn-On Delay Time	$t_{\text{d}(\text{on})}$	$V_{\text{DD}}=15\text{V}, I_{\text{D}}=1.9\text{A}, V_{\text{GS}}=10\text{V}, R_{\text{G}}=6\Omega$ <small>(Note 1,2)</small>	-	2	-	ns
Turn-On Rise Time	t_{r}		-	38	-	
Turn-Off Delay Time	$t_{\text{d}(\text{off})}$		-	812	-	
Turn-Off Fall Time	t_{f}		-	64	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	I_s	---	-	-	0.5	A
Diode Forward Voltage	V_{SD}	$I_s=1.0\text{A}, V_{\text{GS}}=0\text{V}$		0.77	1.2	V

NOTES :

1. Pulse width $\leq 300\mu\text{s}$, Duty cycle $\leq 2\%$
2. Essentially independent of operating temperature typical characteristics.
3. R_{QJA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper
4. The maximum current rating is package limited

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TYPICAL CHARACTERISTIC CURVES

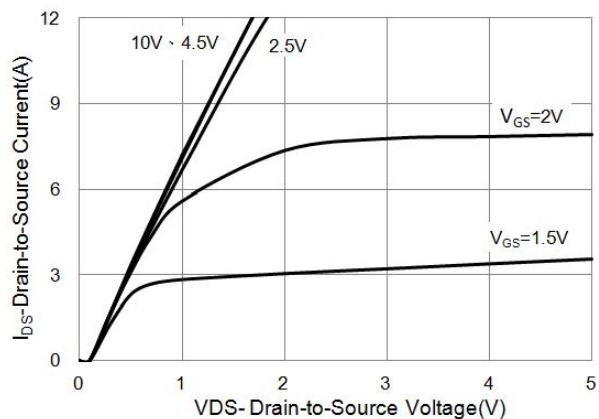


Fig.1 On-Region Characteristics

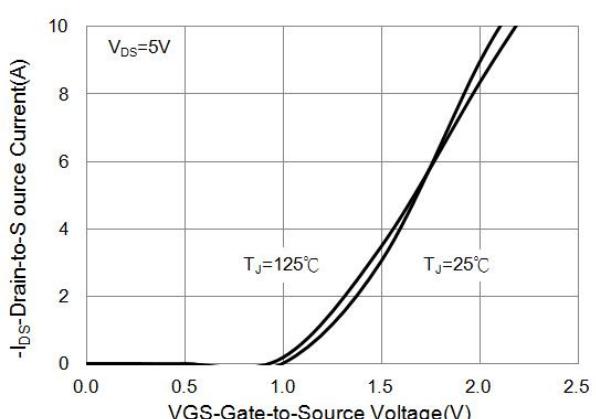


Fig.2 Transfer Characteristics

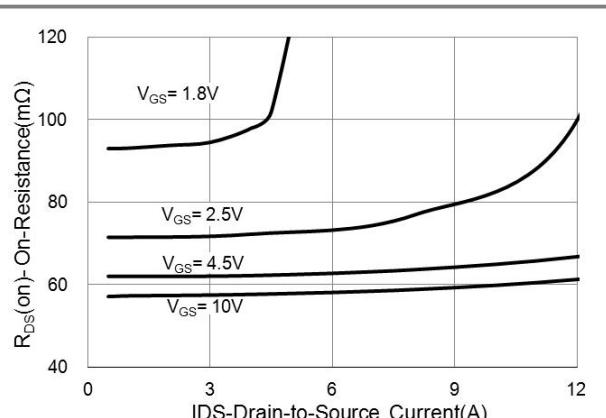


Fig.3 On-Resistance vs. Drain Current

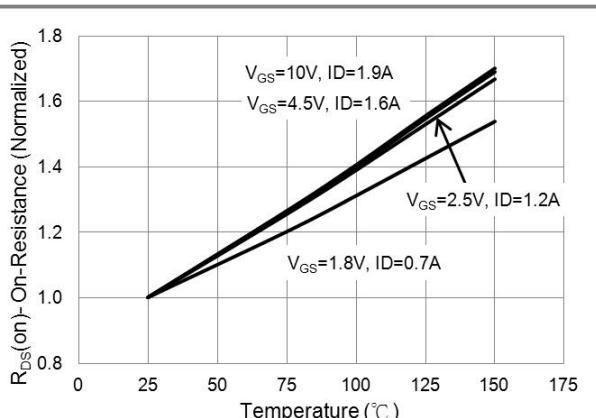


Fig.4 On-Resistance vs. Junction temperature

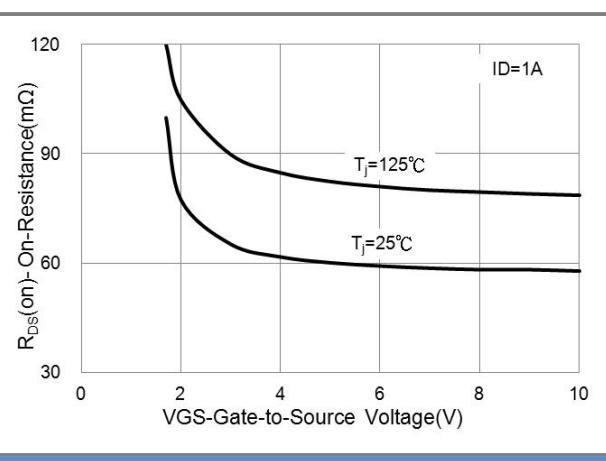


Fig.5 On-Resistance Variation with VGS.

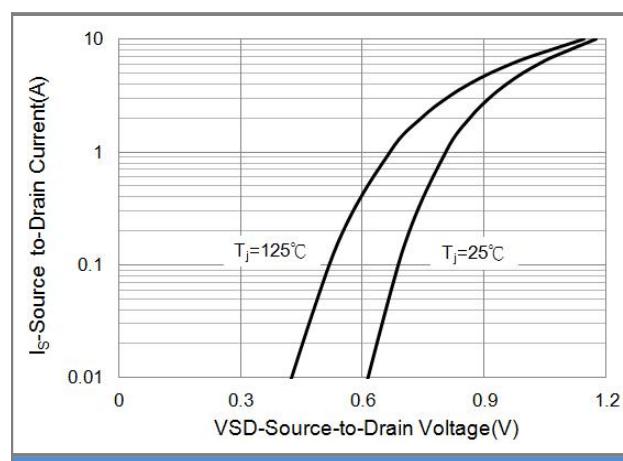


Fig.6 Body Diode Characteristics

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TYPICAL CHARACTERISTIC CURVES

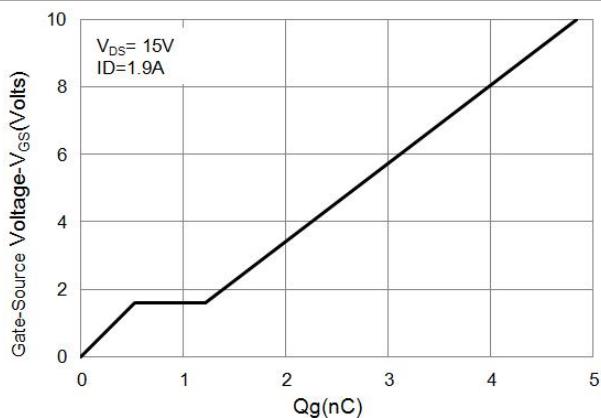


Fig.7 Gate-Charge Characteristics

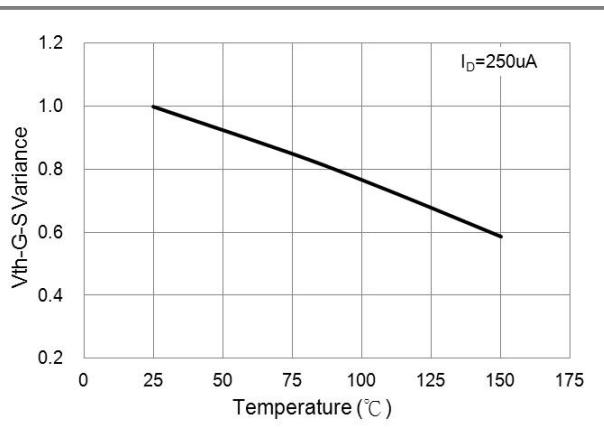


Fig.8 Threshold Voltage Variation with Temperature.

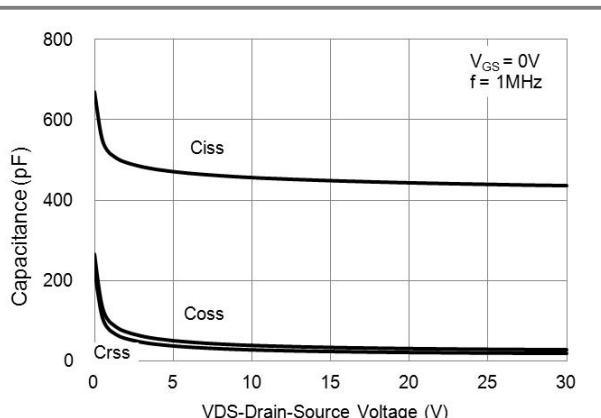
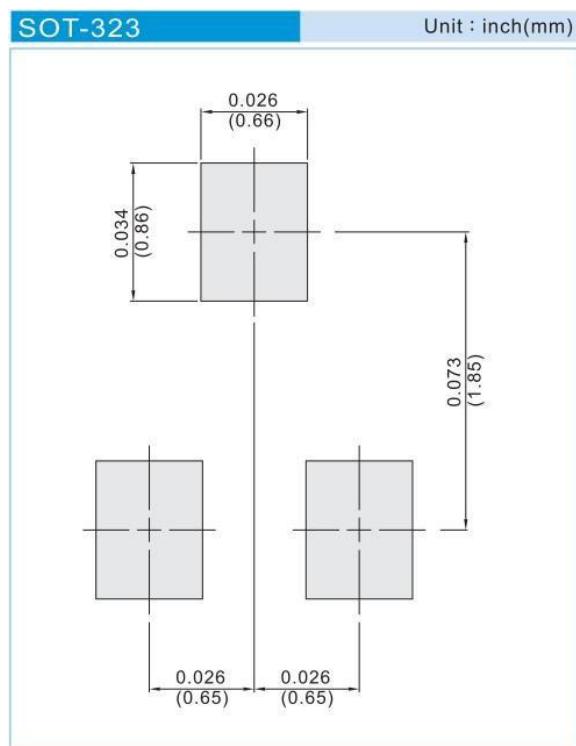


Fig.9 Capacitance vs. Drain-Source Voltage.

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MOUNTING PAD LAYOUT



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