

CSM320N7S223

30V N-Channel Enhancement Mode MOSFET

Voltage

30 V

Current

7 A

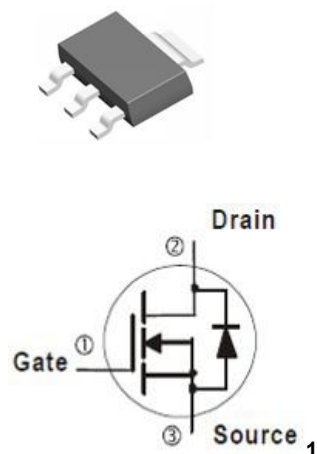
Features

- $R_{DS(ON)}$, $V_{GS}@10V$, $I_D@5.6A < 25m\Omega$
- $R_{DS(ON)}$, $V_{GS}@4.5V$, $I_D@3.5A < 30m\Omega$
- Advanced Trench Process Technology
- Specially Designed for Switch Load, PWM Application, etc.

Mechanical Data

- Case: SOT-223 Package
- Terminals: Solderable per MIL-STD-750, Method 2026

SOT-223



Maximum Ratings and Thermal Characteristics ($T_A=25^{\circ}C$ unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V_{DS}	30	V
Gate-Source Voltage		V_{GS}	+20	V
Continuous Drain Current	$T_C=25^{\circ}C$	I_D	7	A
	$T_C=100^{\circ}C$		4.6	
Pulsed Drain Current		I_{DM}	28	A
Power Dissipation	$T_C=25^{\circ}C$	P_D	3.0	W
	$T_C=100^{\circ}C$		1.2	
Continuous Drain Current	$T_A=25^{\circ}C$	I_D	5.0	A
	$T_A=70^{\circ}C$		4.0	
Power Dissipation	$T_A=25^{\circ}C$	P_D	1.5	W
	$T_A=70^{\circ}C$		0.94	
Operating Junction and Storage Temperature Range		T_J, T_{STG}	-55~150	$^{\circ}C$
Typical Thermal Resistance (Note 3)	Junction to Case	$R_{\theta JC}$	41.6	$^{\circ}C/W$
	Junction to Ambient	$R_{\theta JA}$	85	

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Electrical Characteristics ($T_A=25^{\circ}\text{C}$ unless otherwise noted)

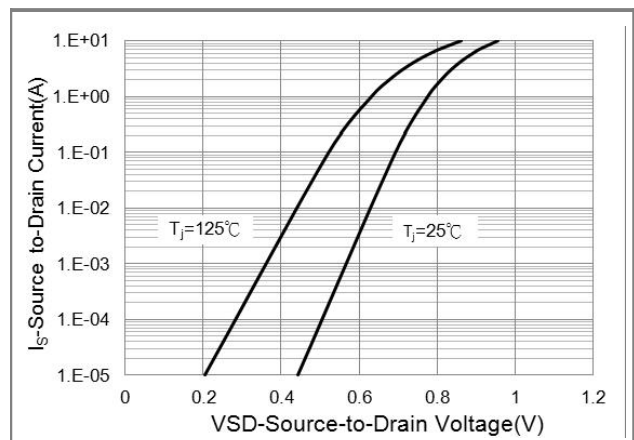
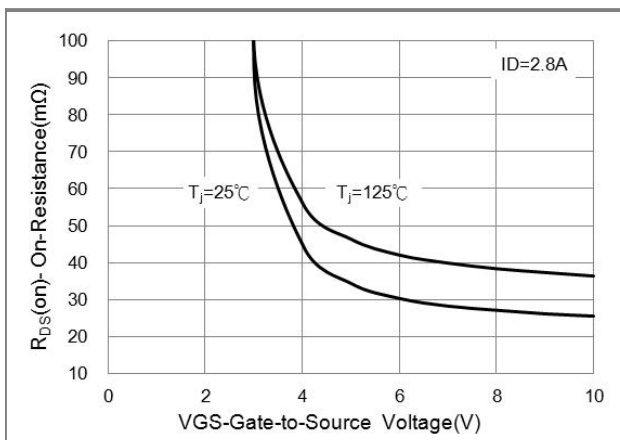
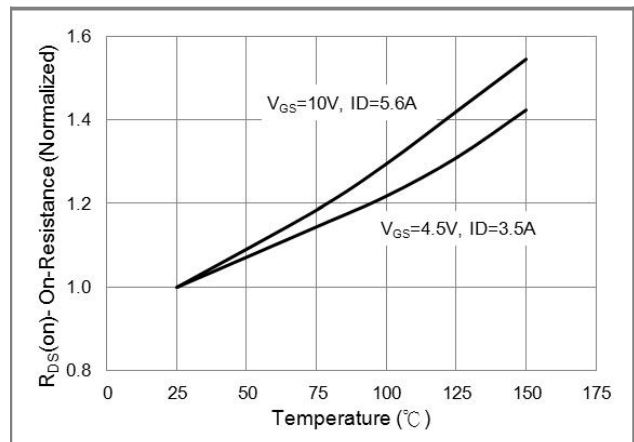
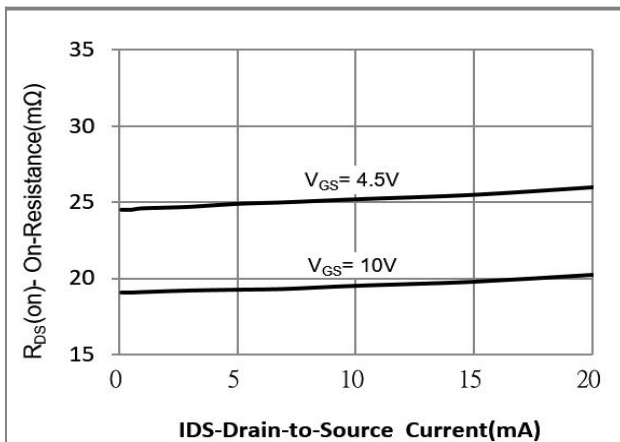
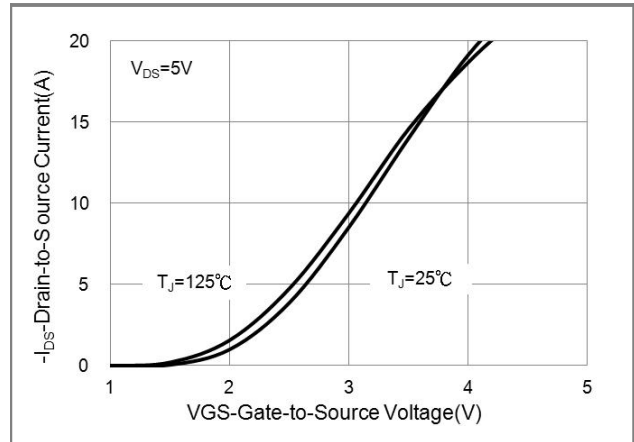
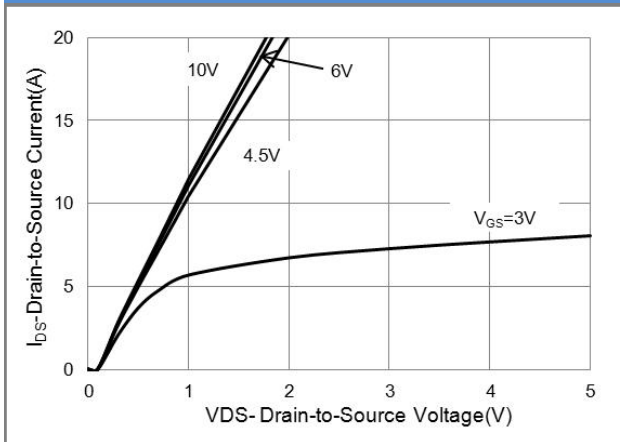
PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =250uA	30	-	-	V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250uA	1.0	1.33	2.1	V
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =10V, I _D =5.6A	-	20	25	mΩ
		V _{GS} =4.5V, I _D =3.5A	-	25	30	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =30V, V _{GS} =0V	-	0.01	1	uA
Gate-Source Leakage Current	I _{GSS}	V _{GS} =±20V, V _{DS} =0V	-	±10	±100	nA
Dynamic						
Total Gate Charge	Q _g	V _{DS} =15V, I _D =5.6A, V _{GS} =10V (Note 1,2)	-	7.8	-	nC
Gate-Source Charge	Q _{gs}		-	1.2	-	
Gate-Drain Charge	Q _{gd}		-	1.5	-	
Input Capacitance	C _{iss}	V _{DS} =15V, V _{GS} =0V, f=1.0MHZ	-	343	-	pF
Output Capacitance	C _{oss}		-	48	-	
Reverse Transfer Capacitance	C _{rss}		-	34	-	
Switching						
Turn-On Delay Time	td _(on)	V _{DD} =15V, I _D =5.6A, V _{GS} =10V, R _G =3Ω (Note 1,2)	-	3	-	ns
Turn-On Rise Time	tr		-	40	-	
Turn-Off Delay Time	td _(off)		-	38	-	
Turn-Off Fall Time	tf		-	39	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	I _S	---	-	-	1.5	A
Diode Forward Voltage	V _{SD}	I _S =1.0A, V _{GS} =0V	-	0.77	1.2	V

NOTES :

1. Pulse width $\leq 300\mu s$, Duty cycle $\leq 2\%$
2. Essentially independent of operating temperature typical characteristics.
3. $R_{\theta JA}$ is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper
4. The maximum current rating is package limited

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TYPICAL CHARACTERISTIC CURVES



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TYPICAL CHARACTERISTIC CURVES

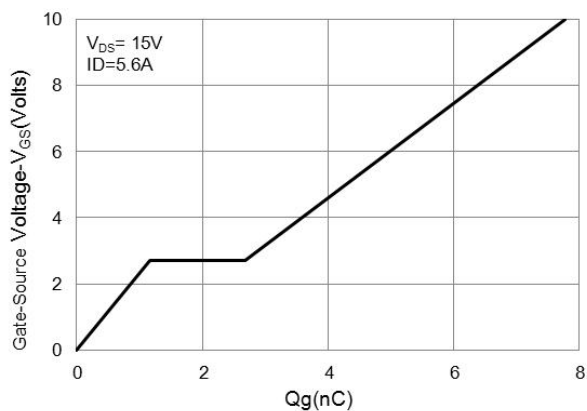


Fig.7 Gate-Charge Characteristics

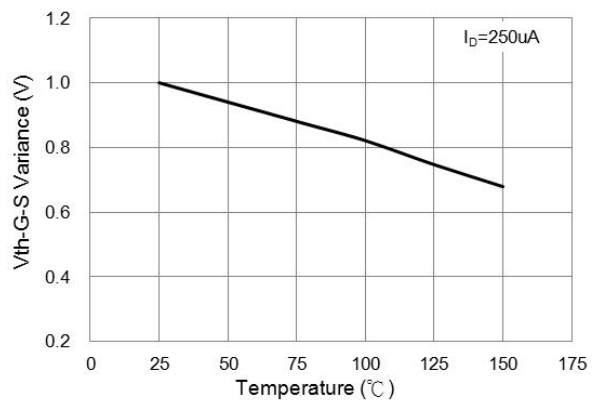


Fig.8 Threshold Voltage Variation with Temperature

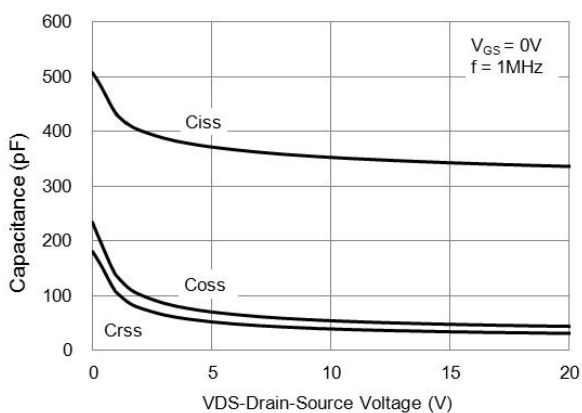
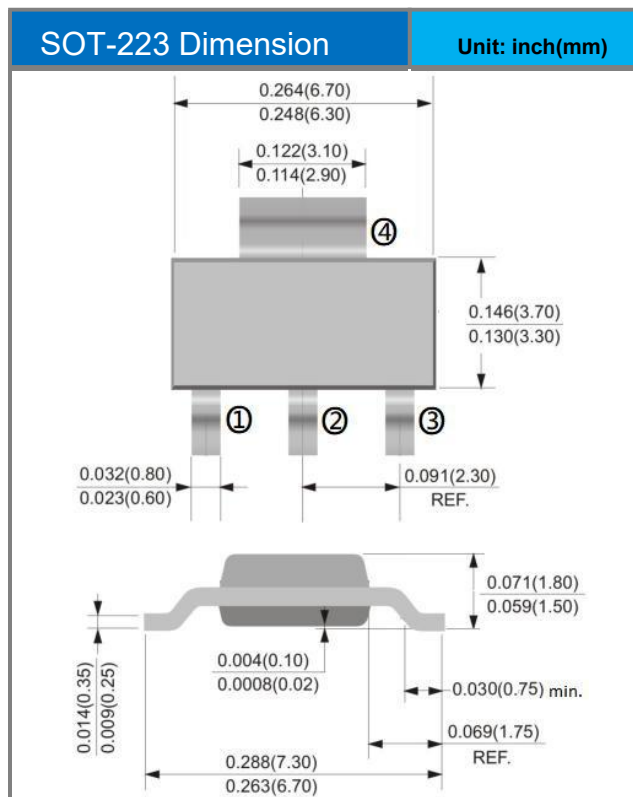


Fig.9 Capacitance vs. Drain-Source Voltage

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Packaging Information & Mounting Pad Layout .

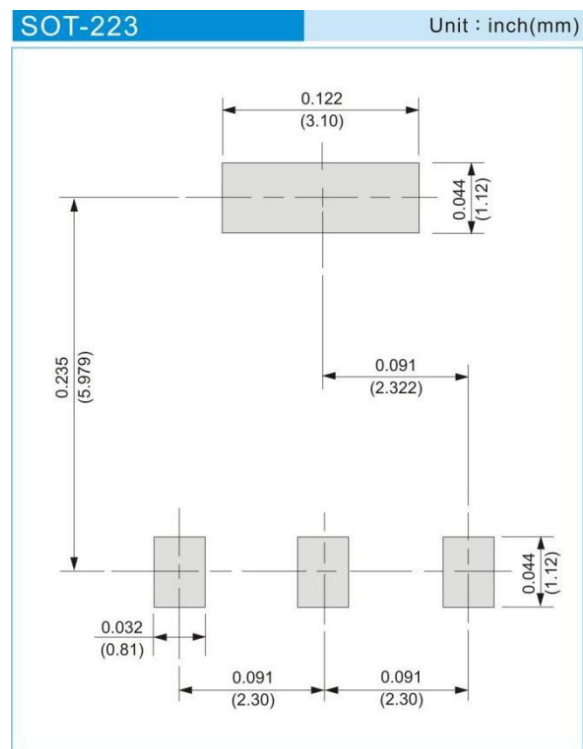


CSM320N7S223

PART NO PACKING CODE VERSION

Part No Packing Code	Package Type	Packing Type
CSM320N7S223	SOT-223	1,000pcs / 13" reel

MOUNTING PAD LAYOUT



CSM320N7S223

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