

60V P-Channel Enhancement Mode MOSFET

Voltage

-60 V

Current

-4A

Features

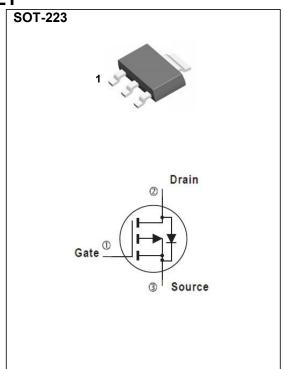
- $R_{DS(ON)}$, V_{GS} @-10V, I_{D} @-2A<170 $m\Omega$
- $R_{DS(ON)}$, V_{GS} @-4.5V, I_{D} @-1.5A<210m Ω
- High switching speed
- Improved dv/dt capability
- Low Gate Charge
- Low reverse transfer capacitance

Mechanical Data

• Case: SOT-223 Package

• Terminals : Solderable per MIL-STD-750, Method 2026

• Approx. Weight: 0.043 ounces, 0.123grams



Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS	
Drain-Source Voltage		V _{DS}	-60	V	
Gate-Source Voltage		V _{GS}	<u>+</u> 20		
Continuous Drain Current (Note 4)	T _A =25°C		-4		
		l _D		A	
Pulsed Drain Current (Note 1)		I _{DM}	-16		
Power Dissipation	T _A =25°C	1	3.1		
		P _D		W	
Single Pulse Avalanche Energy (Note 6)		E _{AS}	32	mJ	
Operating Junction and Storage Temperature Range		T_{J}, T_{STG}	-55~150	°C	
Typical Thermal Resistance					
- Junction to Ambient (Note 4,5)		$R_{\theta JA}$	40.3	°C/W	

Limited only By Maximum Junction Temperature



Electrical Characteristics (T_A=25 °C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV_{DSS}	V _{GS} =0V, I _D =-250uA	-60	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	V _{DS} =V _{GS} , I _D =-250uA	-1	-1.88	-2.5	
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =-10V, I _D =-2A	-	140	170	mΩ
		V _{GS} =-4.5V, I _D =-1.5A	-	190	210	
Zero Gate Voltage Drain Current	I_{DSS}	V _{DS} =-60V, V _{GS} =0V	-	-	-1	uA
Gate-Source Leakage Current	I _{GSS}	V _{GS} = <u>+</u> 20V, V _{DS} =0V	-	-	<u>+</u> 100	nA
Dynamic (Note 7)						
Total Gate Charge	Qg	V _{DS} =-30V, I _D =-2A, V _{GS} =-10V ^(Note 2,3)	-	8.3	-	nC
Gate-Source Charge	Q _{gs}		-	1.8	-	
Gate-Drain Charge	Q_{gd}		-	1.6	-	
Input Capacitance	Ciss	V _{DS} =-30V, V _{GS} =0V, f=1MHZ	-	430	-	pF
Output Capacitance	Coss		-	33	-	
Reverse Transfer Capacitance	Crss		-	29	-	
Turn-On Delay Time	td _(on)	V _{DD} =-30V, I _D =-1A, V _{GS} =-10V,	-	5.1	-	
Turn-On Rise Time	tr		-	20	-	ns
Turn-Off Delay Time	td _(off)		-	36	-	
Turn-Off Fall Time	t _f	$R_G=6\Omega$ (Note 2,3)	-	11	-	
Drain-Source Diode						
Maximum Continuous Drain-Source			-	-	-2	А
Diode Forward Current	Is					
Diode Forward Voltage	V _{SD}	I _S =-1A, V _{GS} =0V	-	-0.78	-1	V

NOTES:

- 1. Pulse width < 300 us, Duty cycle < 2%
- 2. Essentially independent of operating temperature typical characteristics.
- 3. Repetitive rating, pulse width limited by junction temperature T_{J(MAX)}=150°C. Ratings are based on low frequency and duty cycles to keep initial T_J =25°C.
- 4. The maximum current rating is package limited.
- 5. R_{OJA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch² with 2oz.square pad of copper.
- 6. The test condition is L=1mH, I_{AS}=-8A, V_{DD}=-25V, V_{GS}=-10V
- 7. Guaranteed by design, not subject to production testing.



TYPICAL CHARACTERISTIC CURVES

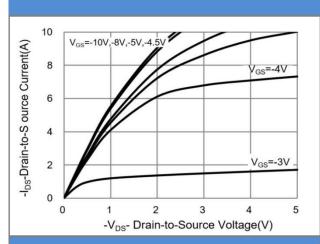


Fig.1 Output Characteristics

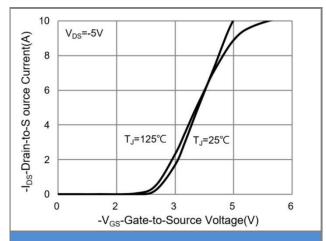


Fig.2 Transfer Characteristics

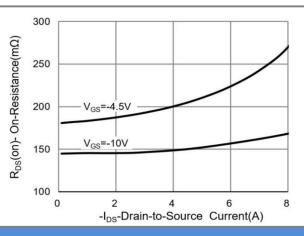


Fig.3 On-Resistance vs. Drain Current

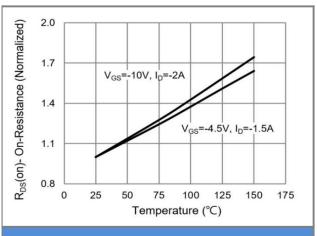


Fig.4 On-Resistance vs. Junction temperature

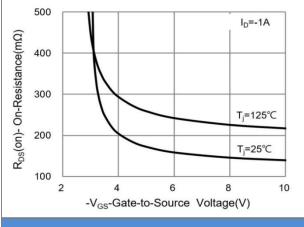


Fig.5 On-Resistance Variation with V_{GS}

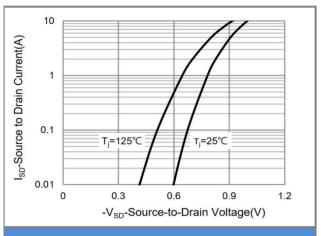


Fig.6 Source-Drain Diode Forward Voltage



TYPICAL CHARACTERISTIC CURVES

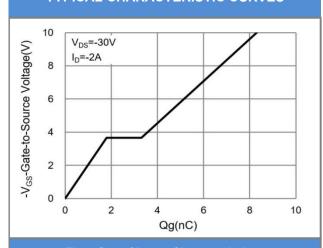


Fig.7 Gate-Charge Characteristics

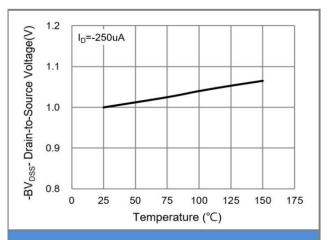


Fig.8 Breakdown Voltage Variation vs. Temperature

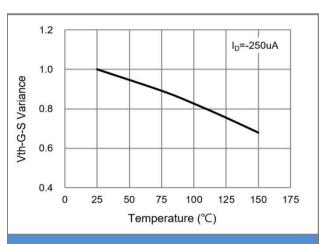


Fig.9 Threshold Voltage Variation with Temperature

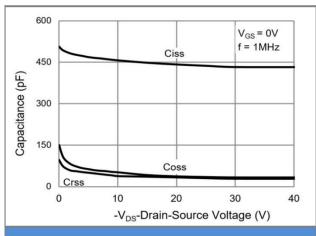


Fig.10 Capacitance vs. Drain-Source Voltage

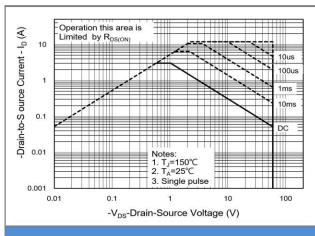


Fig.11 Maximum Safe Operating Area

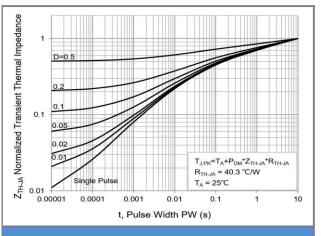


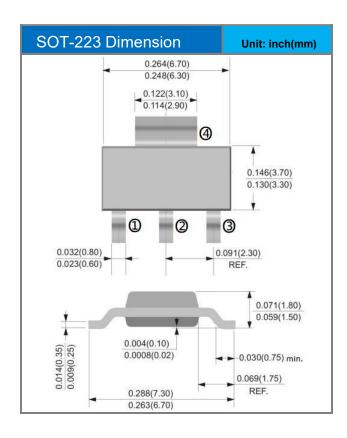
Fig.12 Normalized Transient Thermal Impedance

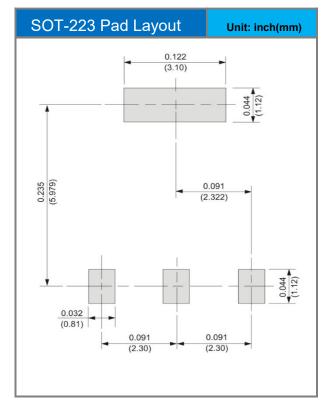


Part No Packing Code Version

Part No Packing Code	Package Type	Packing Type
CSM620P4S223	SOT-223	1,000pcs / 13" reel

Packaging Information & Mounting Pad Layout







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