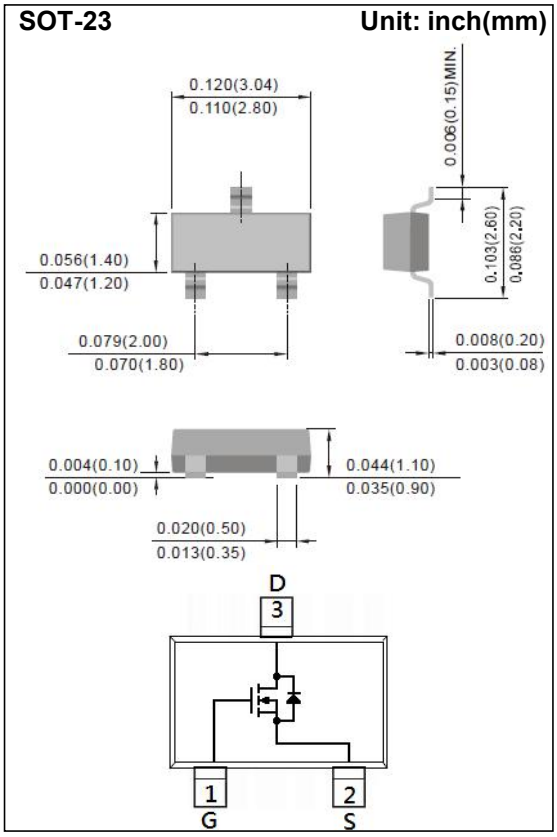
CAN CIRCLE SEMI

CSM1020N2S23

100V N-Channel Enhancement Mode MOSFET

Voltage	100 V	Current	2A



Features

- $R_{DS(ON)}$, V_{GS}@10V, I_D@1.3A<280mΩ
- $R_{DS(ON)}$, V_{GS}@4.5V, I_D@0.6A<300mΩ
- Advanced Trench Process Technology
- Specially Designed for Switch Load, PWM Application, etc

Mechanical Data

- Case: SOT-23 Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 0.0003 ounces, 0.0084 grams

Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V_{DS}	100	V
Gate-Source Voltage		V_{GS}	±20	
Continuous Drain Current (Note 4)	T_A=25°C	I_D	2	A
	T_A=70°C		1	
Pulsed Drain Current (Note 1)		I_{DM}	5	
Power Dissipation	T_A=25°C	P_D	1.25	W
	T_A=70°C		0.8	
Operating Junction and Storage Temperature Range		T_J,T_{STG}	-55~150	°C
Typical Thermal Resistance - Junction to Ambient (Note 3,4)		$R_{\theta JA}$	100	°C/W

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Electrical Characteristics (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV_{DSS}	V_{GS}=0V, I_D=250uA	100	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS}=V_{GS}, I_D=250uA	1	2.06	2.5	
Drain-Source On-State Resistance	$R_{DS(on)}$	V_{GS}=10V, I_D=1.3A	-	270	280	mΩ
		V_{GS}=4.5V, I_D=0.6A	-	290	300	
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS}=80V, V_{GS}=0V	-	-	1	uA
Gate-Source Leakage Current	I_{GSS}	V_{GS}=±20V, V_{DS}=0V	-	-	±100	nA
Dynamic (Note 6)						
Total Gate Charge	Q_g	V_{DS}=50V, I_D=1.3A, V_{GS}=10V (Note 2,3)	-	9.1	-	nC
Gate-Source Charge	Q_{gs}		-	2.1	-	
Gate-Drain Charge	Q_{gd}		-	1.4	-	
Input Capacitance	Ciss	V_{DS}=30V, V_{GS}=0V, f=1MHZ	-	508	-	pF
Output Capacitance	Coss		-	29	-	
Reverse Transfer Capacitance	Crss		-	18	-	
Turn-On Delay Time	$td_{(on)}$	V_{DD}=50V, I_D=1A, V_{GS}=10V, R_G=6Ω (Note 2,3)	-	2	-	ns
Turn-On Rise Time	tr		-	21	-	
Turn-Off Delay Time	$td_{(off)}$		-	12	-	
Turn-Off Fall Time	tf		-	19	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current (Note 4)	I_S	---	-	-	1.5	A
Diode Forward Voltage	V_{SD}	I_S=1A, V_{GS}=0V	-	0.78	1.2	V

NOTES :

1. Pulse width≤300us, Duty cycle≤2%.
2. Essentially independent of operating temperature typical characteristics.
3. Repetitive rating, pulse width limited by junction temperature $T_{J(MAX)}$=150°C. Ratings are based on low frequency and duty cycles to keep initial T_J =25°C.
4. The maximum current rating is package limited.
5. $R_{\Theta JA}$ is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad ofcopper.
6. Guaranteed by design, not subject to production testing.

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TYPICAL CHARACTERISTIC CURVES

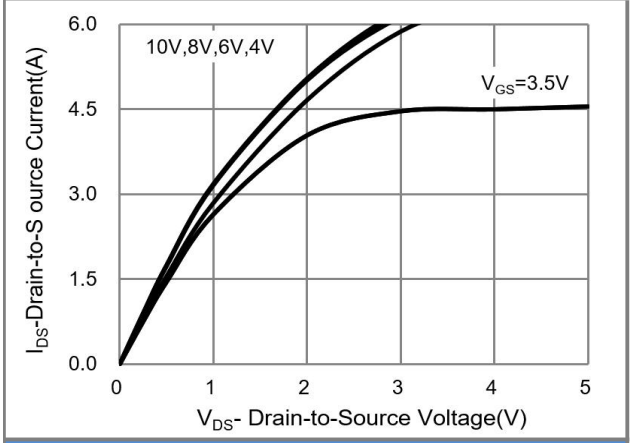

Fig.1 On-Region Characteristics

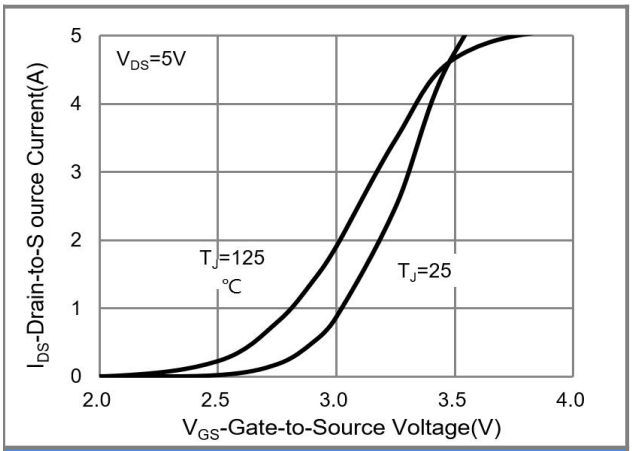

Fig.2 Transfer Characteristics

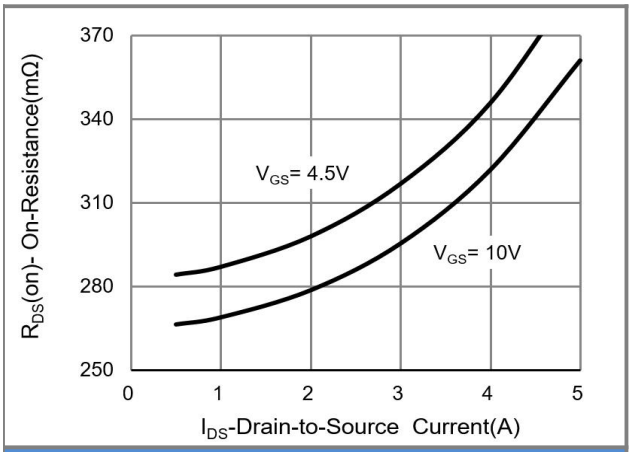

Fig.3 On-Resistance vs. Drain Current

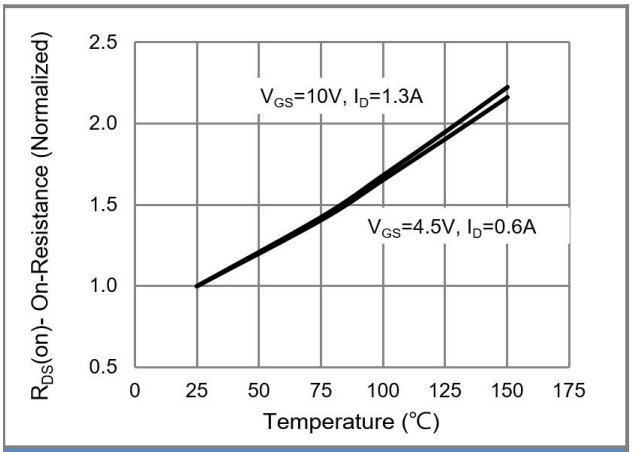

Fig.4 On-Resistance vs. Junction temperature

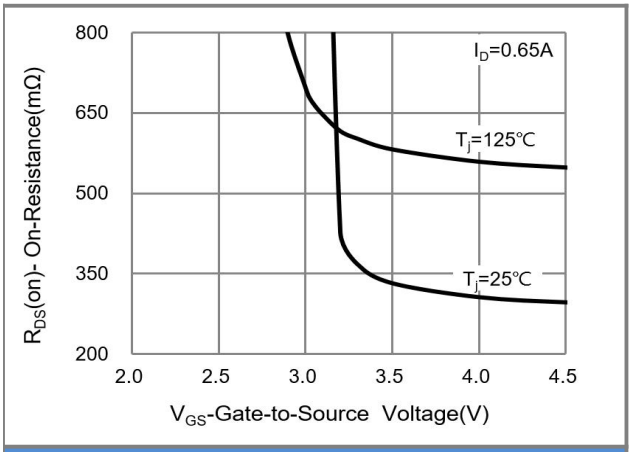

Fig.5 On-Resistance Variation with V_{GS}

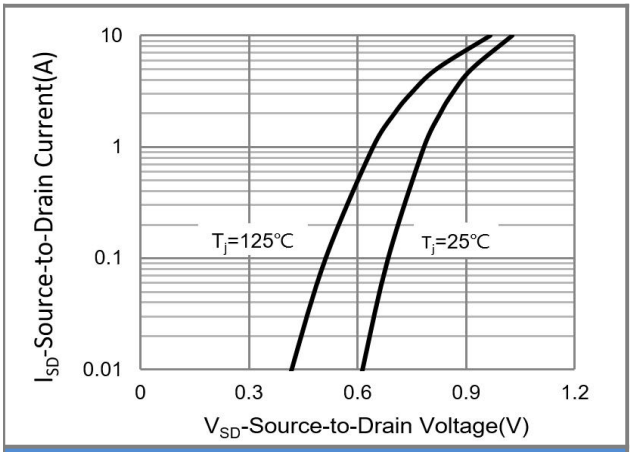

Fig.6 Body Diode Characteristics

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TYPICAL CHARACTERISTIC CURVES

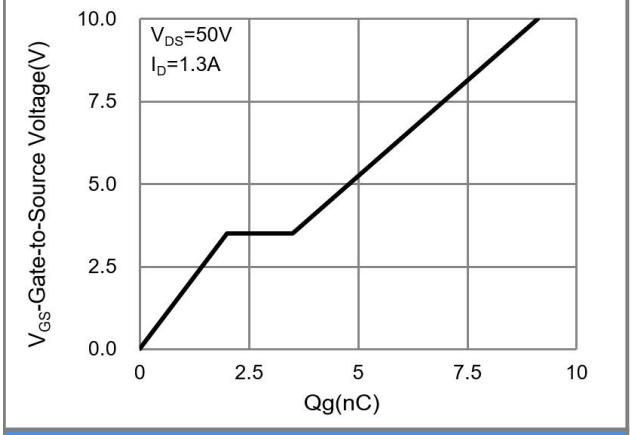


Fig.7 Gate-Charge Characteristics

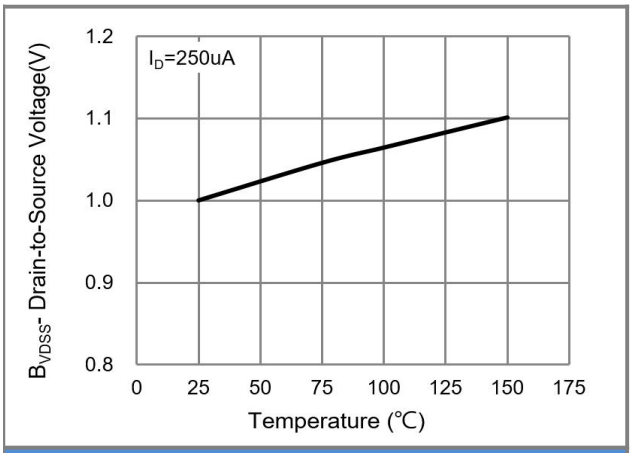


Fig.8 Breakdown Voltage Variation vs. Temperature

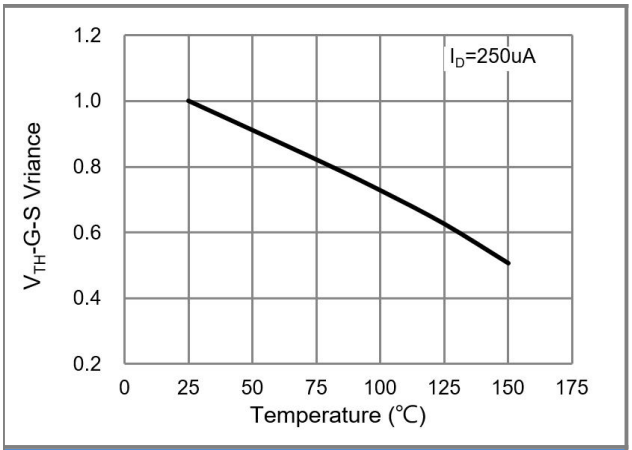


Fig.9 Threshold Voltage Variation with Temperature

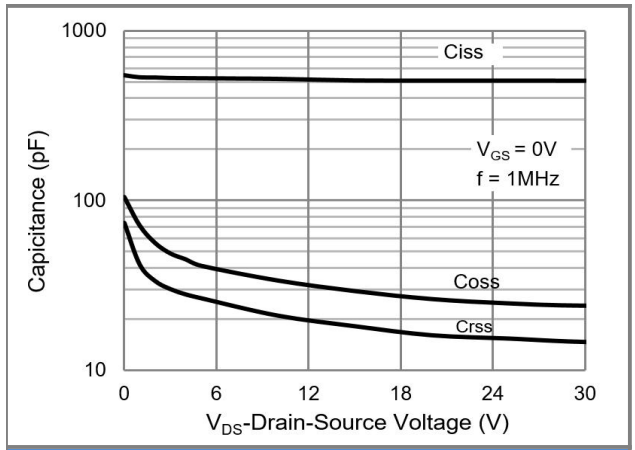


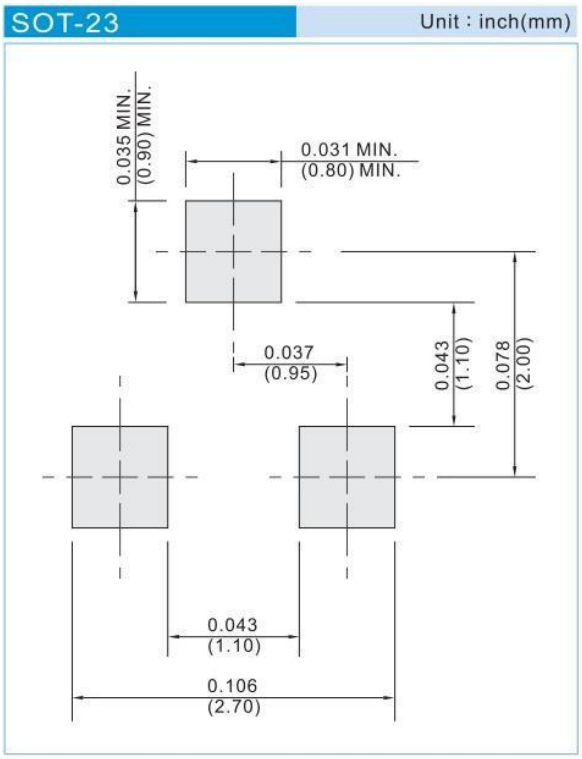
Fig.10 Capacitance vs. Drain-Source Voltage

CSM1020N2S23

Part No Packing Code Version

Part No Packing Code	Package Type	Packing Type
CSM1020N2S23	SOT-23	3K pcs / 7” reel

Mounting Pad Layout



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