

CSM212P6S23

20V P-Channel Enhancement Mode MOSFET

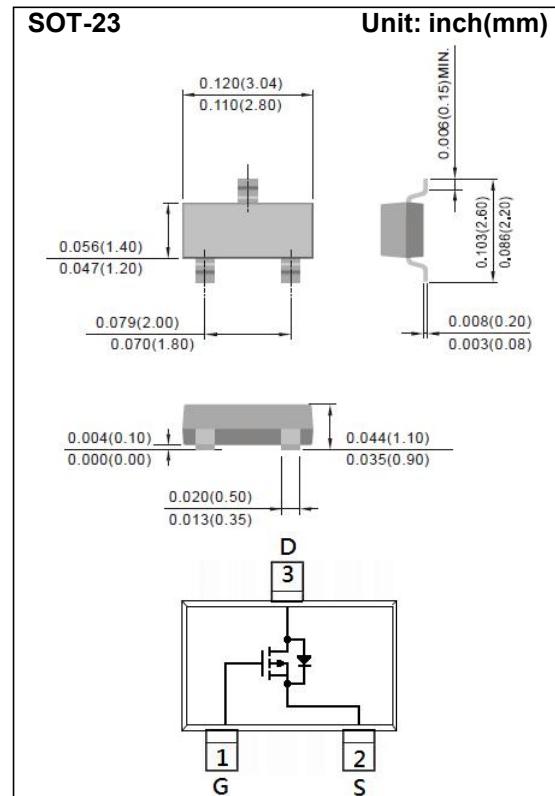
Voltage -20 V Current -6A

Features

- RDS(ON) , V_{GS} @-4.5V, I_D @-4.0A<30mΩ
- RDS(ON) , V_{GS} @-2.5V, I_D @-3.0A<40mΩ
- RDS(ON) , V_{GS} @-1.8V, I_D @-2.0A<52mΩ
- Advanced Trench Process Technology
- Specially Designed for Switch Load, PWM Application, etc.

Mechanical Data

- Case: SOT-23 Package
- Terminals: Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 0.0003 ounces, 0.0084 grams



Maximum Ratings and Thermal Characteristics ($T_A=25^\circ\text{C}$ unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNITS
Drain-Source Voltage	V_{DS}	-20	V
Gate-Source Voltage	V_{GS}	± 12	V
Continuous Drain Current	I_D	-6	A
Pulsed Drain Current	I_{DM}	-24	A
Power Dissipation $T_a=25^\circ\text{C}$	P_D	1.25	W
		10	mW/ °C
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55~150	°C
Typical Thermal Resistance - Junction to Ambient (Note 3)	$R_{\theta JA}$	100	°C/W

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Electrical Characteristics ($T_A=25^\circ\text{C}$ unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=-250\mu\text{A}$	-20	-	-	V
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=-250\mu\text{A}$	-0.3	-0.5	-1	V
Drain-Source On-State Resistance	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}}=-4.5\text{V}, I_{\text{D}}=-4.0\text{A}$	-	27	30	$\text{m}\Omega$
		$V_{\text{GS}}=-2.5\text{V}, I_{\text{D}}=-3.0\text{A}$	-	33	40	
		$V_{\text{GS}}=-1.8\text{V}, I_{\text{D}}=-2.0\text{A}$	-	41	52	
Zero Gate Voltage Drain Current	I_{DSS}	$V_{\text{DS}}=-20\text{V}, V_{\text{GS}}=0\text{V}$	-	-	-1	μA
Gate-Source Leakage Current	I_{GSS}	$V_{\text{GS}}=\pm 10\text{V}, V_{\text{DS}}=0\text{V}$	-	-	± 100	nA
Dynamic (Note 5)						
Total Gate Charge	Q_g	$V_{\text{DS}}=-10\text{V}, I_{\text{D}}=-1.0\text{A}, V_{\text{GS}}=-4.5\text{V}$ (Note 1,2)	-	14	-	nC
Gate-Source Charge	Q_{gs}		-	1.5	-	
Gate-Drain Charge	Q_{gd}		-	2.9	-	
Input Capacitance	C_{iss}	$V_{\text{DS}}=-15\text{V}, V_{\text{GS}}=0\text{V}, f=1.0\text{MHz}$	-	1237	-	pF
Output Capacitance	C_{oss}		-	155	-	
Reverse Transfer Capacitance	C_{rss}		-	133	-	
Turn-On Delay Time	$t_{\text{d}(\text{on})}$	$V_{\text{DD}}=-10\text{V}, I_{\text{D}}=-1.0\text{A}, V_{\text{GS}}=-4.5\text{V}, R_{\text{G}}=25\Omega$ (Note 1,2)	-	8.1	-	ns
Turn-On Rise Time	t_r		-	32	-	
Turn-Off Delay Time	$t_{\text{d}(\text{off})}$		-	207	-	
Turn-Off Fall Time	t_f		-	114	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	I_s	---	-	-	-5.2	A
Diode Forward Voltage	V_{SD}	$I_s=-1.0\text{A}, V_{\text{GS}}=0\text{V}$	-	-0.75	-1.2	V

NOTES :

1. Pulse width<300us, Duty cycle<2%
2. Essentially independent of operating temperature typical characteristics.
3. R_{OJA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper
4. The maximum current rating is package limited
5. Guaranteed by design, not subject to production testing

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TYPICAL CHARACTERISTIC CURVES

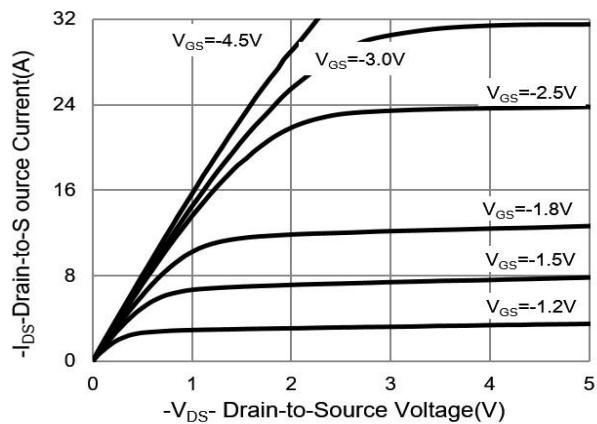


Fig.1 On-Region Characteristics

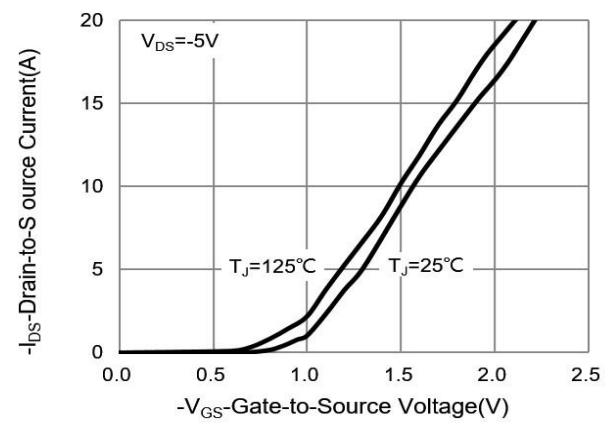


Fig.2 Transfer Characteristics

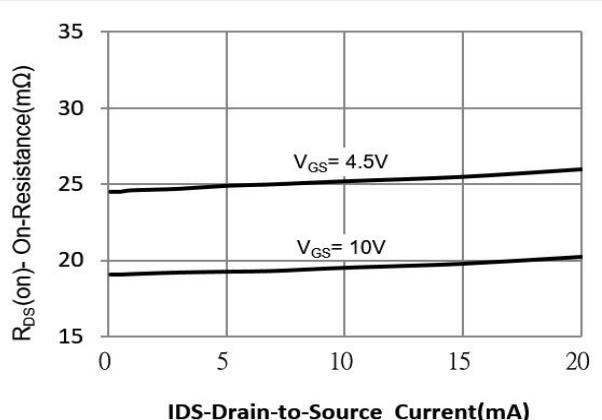


Fig.3 On-Resistance vs. Drain Current

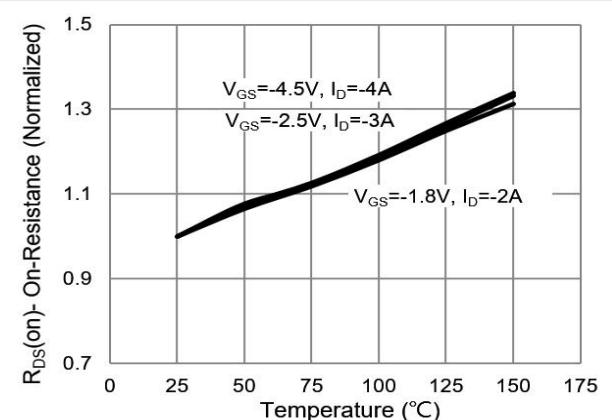


Fig.4 On-Resistance vs. Junction temperature

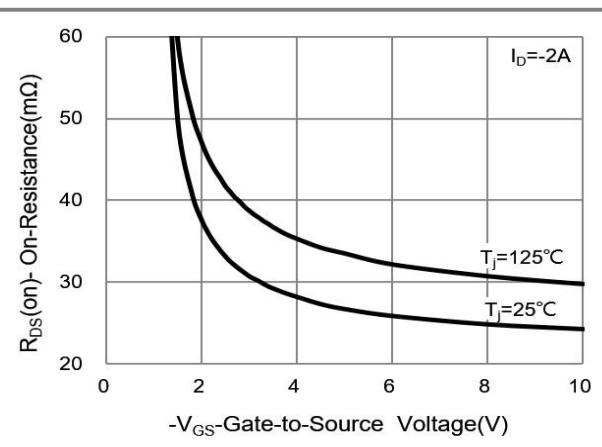


Fig.5 On-Resistance Variation with VGS.

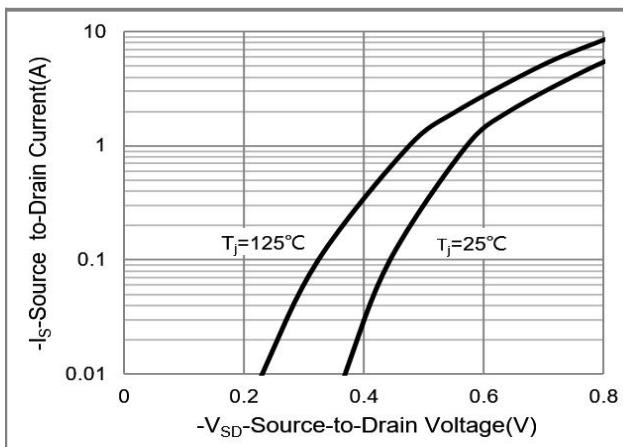


Fig.6 Body Diode Characteristics

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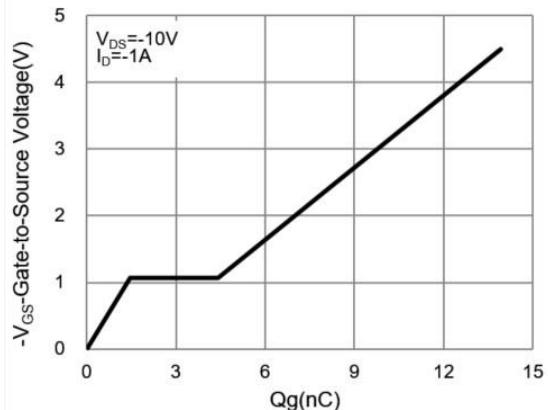


Fig.7 Gate-Charge Characteristics

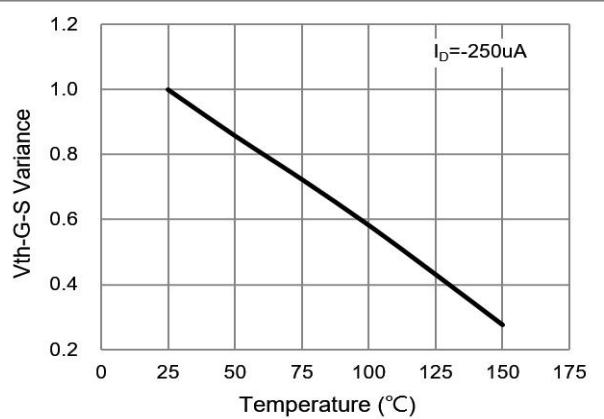


Fig.8 Threshold Voltage Variation with Temperature.

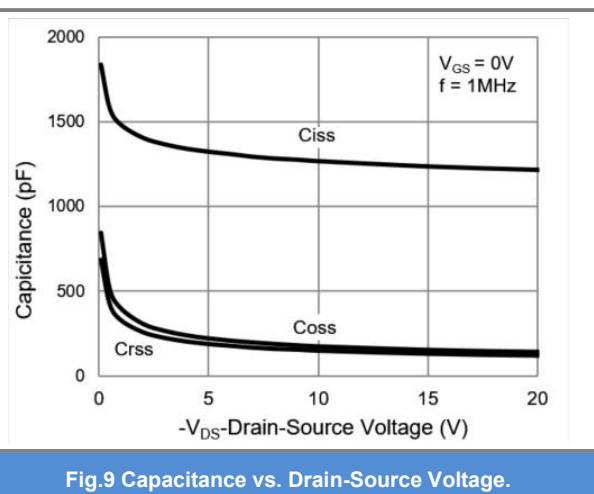


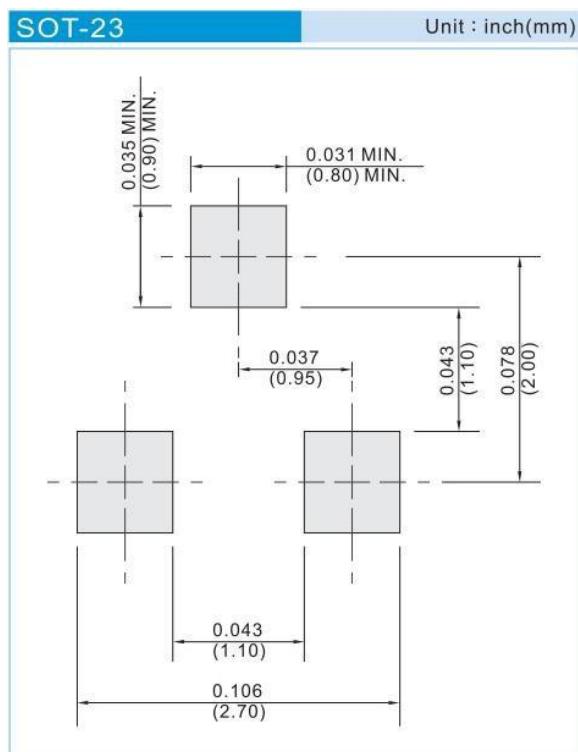
Fig.9 Capacitance vs. Drain-Source Voltage.

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PART NO PACKING CODE VERSION

Part No Packing Code	Package Type	Packing Type
CSM212P6S23	SOT-23	3K pcs / 7" reel

MOUNTING PAD LAYOUT



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