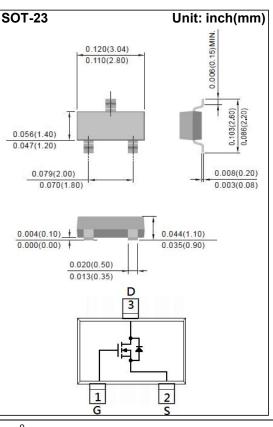
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# CSM620N3S23 60V N-Channel Enhancement Mode MOSFET Voltage 60 V Current 3A

#### Features

- RDS(ON), VGS@10V, ID@2.0A<60mΩ
- RDS(ON), VGS@4.5V, ID@1.0A<75m $\Omega$
- Advanced Trench Process Technology
- Specially Designed for Switch Load, PWM Application, etc



- Mechanical Data
- Case: SOT-23 Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 0.0003 ounces, 0.0084 grams

#### Maximum Ratings and Thermal Characteristics (T<sub>A</sub>=25<sup>°</sup>C unless otherwise noted)

| PARAME  | SYMBOL               | LIMIT            | UNITS       |        |
|---|----------------------|------------------|-------------|--------|
| Drain-Source Voltage  |                      | V <sub>DS</sub>  | 60          | V      |
| Gate-Source Voltage   |                      | V <sub>GS</sub>  | <u>+</u> 20 | V      |
| Continuous Drain Current  |                      | ID               | 3           | Α      |
| Pulsed Drain Current (Note 4)   |                      | I <sub>DM</sub>  | 12          | А      |
| Power Dissipation   | T <sub>a</sub> =25°C |                  | 1.25        | W      |
|   | Derate above 25°C    | PD               | 10          | mW/ °C |
| Operating Junction and Storage Temperature Range                        |                      | $T_{J}, T_{STG}$ | -55~150     | °C     |
| Typical Thermal resistance<br>- Junction to Ambient <sup>(Note 3)</sup> |                      | R <sub>eja</sub> | 100         | °C/W   |



#### Electrical Characteristics (T<sub>A</sub>=25°C unless otherwise noted)

| PARAMETER                        | SYMBOL              | TEST CONDITION  | MIN. | TYP. | MAX.         | UNITS |
|----------------------------------|---------------------|---|------|------|--------------|-------|
| Static                           |                     |   | _    |      |              |       |
| Drain-Source Breakdown Voltage   | $BV_{DSS}$          | $V_{GS}$ =0V, I <sub>D</sub> =250uA   | 60   | -    | -            | V     |
| Gate Threshold Voltage           | $V_{GS(th)}$        | $V_{DS}=V_{GS}$ , $I_{D}=250$ uA  | 1.0  | 1.75 | 2.5          | V     |
| Drain-Source On-State Resistance | R <sub>DS(on)</sub> | $V_{GS}$ =10V, $I_{D}$ =2.0A  | -    | 55   | 60           | mΩ    |
|                                  |                     | V <sub>GS</sub> =4.5V, I <sub>D</sub> =1.0A   | -    | 60   | 75           |       |
| Zero Gate Voltage Drain Current  | I <sub>DSS</sub>    | V <sub>DS</sub> =48V, V <sub>GS</sub> =0V   | -    | -    | 1            | uA    |
| Gate-Source Leakage Current      | I <sub>GSS</sub>    | V <sub>GS</sub> = <u>+</u> 20V, V <sub>DS</sub> =0V                                       | -    | -    | <u>+</u> 100 | nA    |
| Dynamic (Note 5)                 |                     | ·   | ·    |      |              |       |
| Total Gate Charge                | Qg                  |   | -    | 9.3  | -            | nC    |
| Gate-Source Charge               | $Q_gs$              | V <sub>DS</sub> =48V, I <sub>D</sub> =2.0A,<br>V <sub>GS</sub> =10V <sup>(Note 1,2)</sup> | -    | 2.2  | -            |       |
| Gate-Drain Charge                | $Q_gd$              |   | -    | 1.9  | -            |       |
| Input Capacitance                | Ciss                | V <sub>DS</sub> =15V, V <sub>GS</sub> =0V,  | -    | 509  | -            | pF    |
| Output Capacitance               | Coss                |   | -    | 47   | -            |       |
| Reverse Transfer Capacitance     | Crss                | f=1.0MHZ  | -    | 23   | -            |       |
| Turn-On Delay Time               | td <sub>(on)</sub>  |   | -    | 3.2  | -            |       |
| Turn-On Rise Time                | tr                  | $V_{DD}=30V, I_{D}=2.0A,$<br>$V_{GS}=10V,$<br>$R_{G}=3.3\Omega^{(Note 1.2)}$              | -    | 9.7  | -            | ns    |
| Turn-Off Delay Time              | td <sub>(off)</sub> |   | -    | 18.5 | -            |       |
| Turn-Off Fall Time               | tf                  | $R_{G}$ -3.3 $\Omega$   | -    | 6.4  | -            |       |
| Drain-Source Diode               |                     |   |      |      |              |       |
| Maximum Continuous Drain-Source  |                     |   |      |      | 2.5          | •     |
| Diode Forward Current            | I <sub>S</sub>      |   | -    | -    | 2.5          | A     |
| Diode Forward Voltage            | $V_{SD}$            | I <sub>S</sub> =1A, V <sub>GS</sub> =0V   | -    | 0.77 | 1.2          | V     |

NOTES :

1. Pulse width <300us, Duty cycle <2%

2. Essentially independent of operating temperature typical characteristics.

3. R<sub>0JA</sub> is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper.

4. The maximum current rating is package limited.

5. Guaranteed by design, not subject to production testing.



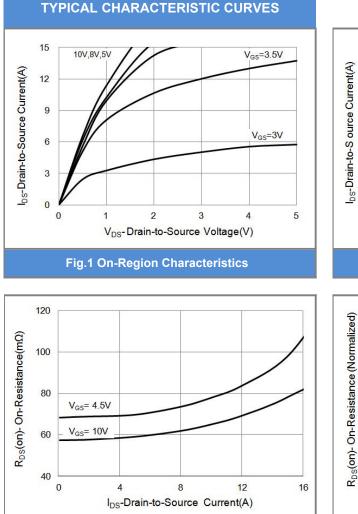
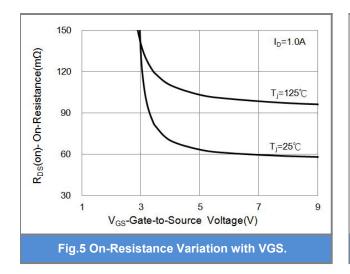
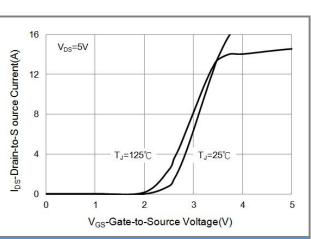


Fig.3 On-Resistance vs. Drain Current





**Fig.2 Transfer Characteristics** 

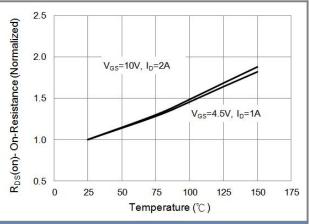
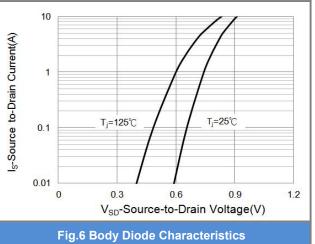
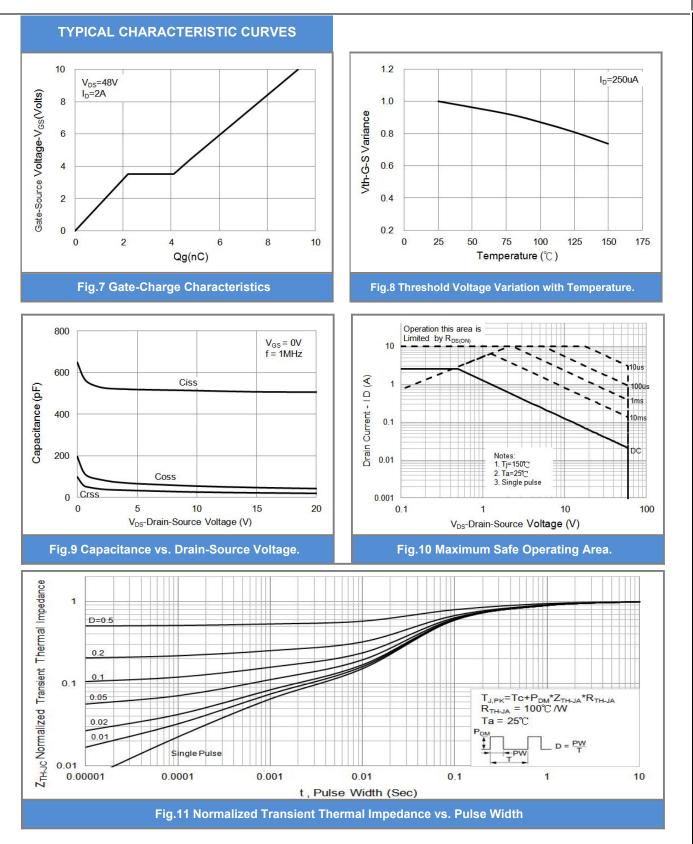


Fig.4 On-Resistance vs. Junction temperature





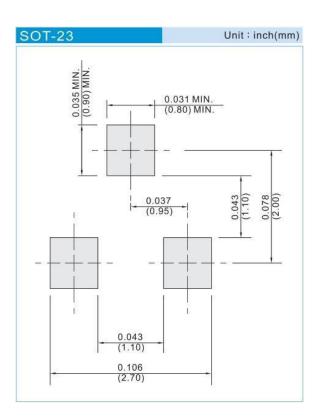




#### PART NO PACKING CODE VERSION

| PART NO PACKING CODE | Package Type | Packing type     |  |  |
|----------------------|--------------|------------------|--|--|
| CSM620N3S23          | SOT-23       | 3K pcs / 7" reel |  |  |

#### MOUNTING PAD LAYOUT





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