

CSM212N8D2-2

20V N-Channel Enhancement Mode MOSFET

Voltage

20 V

Current

8A

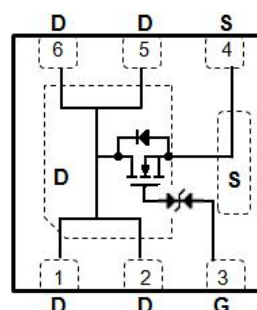
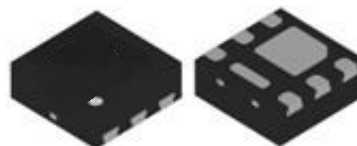
Features

- $R_{DS(ON)}$, $V_{GS}@10V$, $I_D@10A < 11.5m\Omega$
- $R_{DS(ON)}$, $V_{GS}@4.5V$, $I_D@6A < 15m\Omega$
- High switching speed
- Improved dv/dt capability
- Low Gate Charge
- Low reverse transfer capacitance
- ESD Protected 2KV HBM

Mechanical Data

- Case : DFN2020B-6L Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight : 0.0003 ounces, 0.0086 grams

DFN2020B-6L



Maximum Ratings and Thermal Characteristics ($T_A=25^\circ\text{C}$ unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V _{DS}	20	V
Gate-Source Voltage		V _{GS}	±12	
Continuous Drain Current (Note 4)		I _D	8	A
Pulsed Drain Current (Note 1)		I _{DM}	32	
Power Dissipation	T _A =25°C	P _D	2	W
	Derate above 25°C		16	mW/ °C
Operating Junction and Storage Temperature Range		T _J , T _{STG}	-55~150	°C
Typical Thermal Resistance		R _{θJA}	62.5	°C/W
- Junction to Ambient (Note 4,5)				

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Electrical Characteristics ($T_A=25^{\circ}\text{C}$ unless otherwise noted)

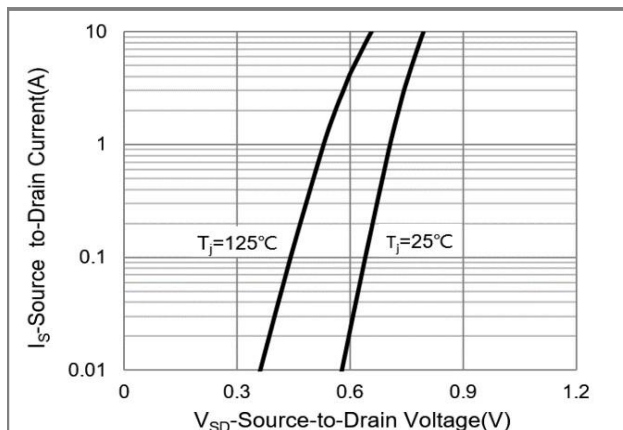
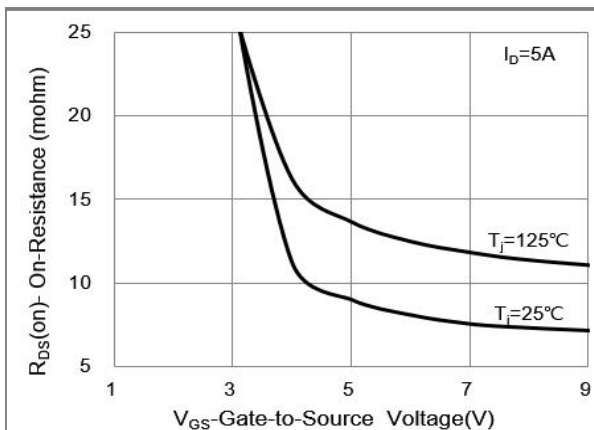
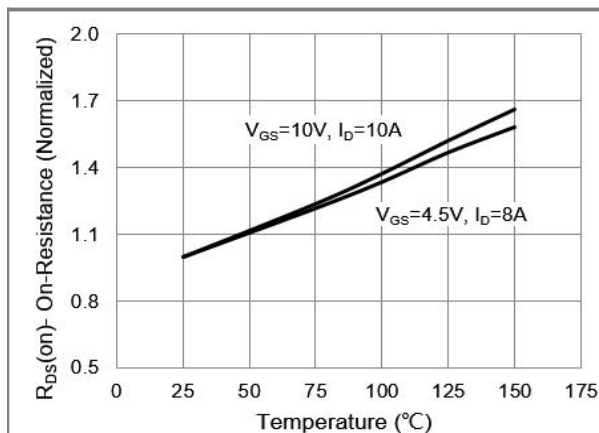
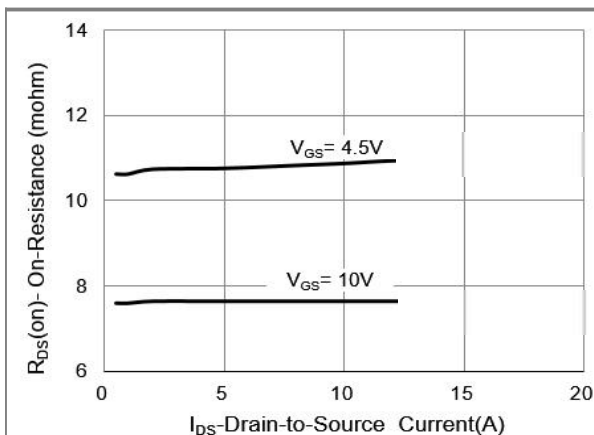
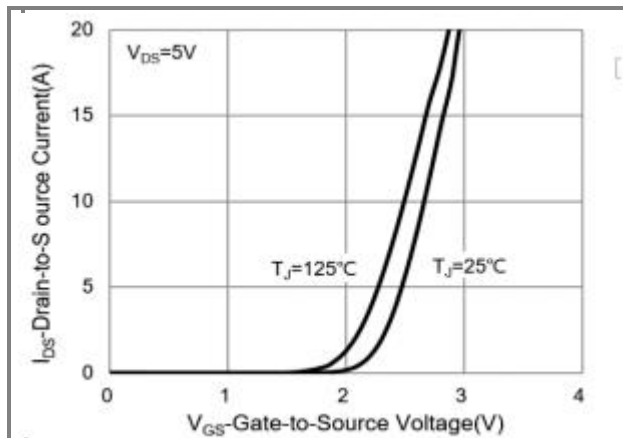
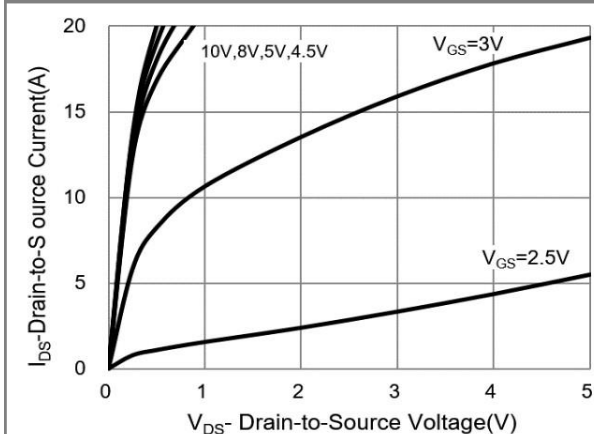
PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=250\mu A$	20	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	1	1.7	2.5	
Drain-Source On-State Resistance	$R_{DS(on)}$	$V_{GS}=10V, I_D=10A$	-	7.5	11.5	mΩ
		$V_{GS}=4.5V, I_D=6A$	-	11	15	
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=20V, V_{GS}=0V$	-	-	1	μA
Gate-Source Leakage Current	I_{GSS}	$V_{GS}=\pm 12V, V_{DS}=0V$	-	-	±100	nA
Dynamic (Note 6)						
Total Gate Charge	Q_g	$V_{DS}=15V, I_D=10A,$ $V_{GS}=4.5V$ (Note 2,3)	-	6.9	-	nC
Gate-Source Charge	Q_{gs}		-	2.7	-	
Gate-Drain Charge	Q_{gd}		-	1.8	-	
Input Capacitance	C_{iss}	$V_{DS}=20V, V_{GS}=0V,$ $f=1MHz$	-	781	-	pF
Output Capacitance	C_{oss}		-	158	-	
Reverse Transfer Capacitance	C_{rss}		-	92	-	
Turn-On Delay Time	$t_{d(on)}$	$V_{DS}=15V, I_D=10A,$ $V_{GS}=10V, R_G=6\Omega$ (Note 2,3)	-	5.4	-	ns
Turn-On Rise Time	t_r		-	86	-	
Turn-Off Delay Time	$t_{d(off)}$		-	20	-	
Turn-Off Fall Time	t_f		-	10	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	I_S	---	-	-	1.5	A
Diode Forward Voltage	V_{SD}	$I_S=1A, V_{GS}=0V$	-	0.73	1	V

NOTES :

1. Pulse width $\leq 300\mu s$, Duty cycle $\leq 2\%$.
2. Essentially independent of operating temperature typical characteristics.
3. Repetitive rating, pulse width limited by junction temperature $T_{J(MAX)}=150^{\circ}\text{C}$. Ratings are based on low frequency and duty cycles to keep initial $T_J=25^{\circ}\text{C}$.
4. The maximum current rating is package limited.
5. $R_{\theta JA}$ is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch² with 2oz.square pad of copper.
6. Guaranteed by design, not subject to production testing.

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TYPICAL CHARACTERISTIC CURVES



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TYPICAL CHARACTERISTIC CURVES

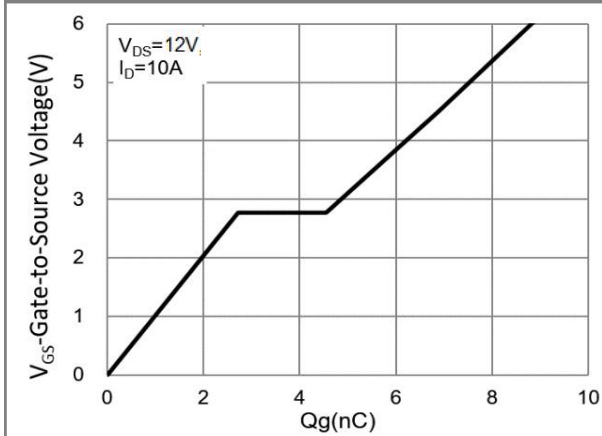


Fig.7 Gate-Charge Characteristics

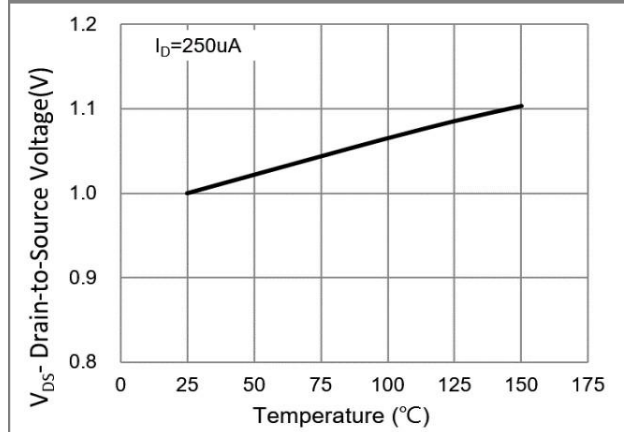


Fig.8 Breakdown Voltage Variation vs. Temperature

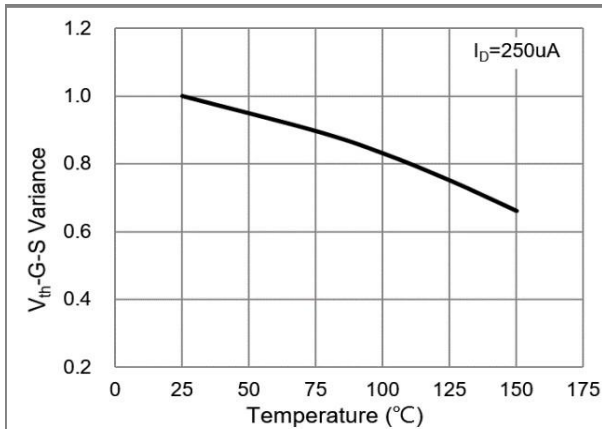


Fig.9 Threshold Voltage Variation with Temperature

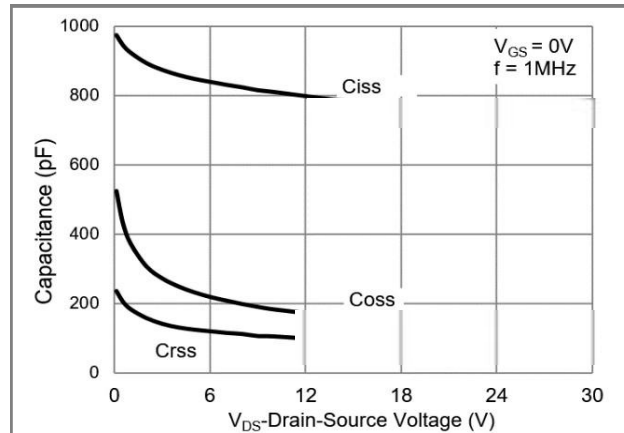


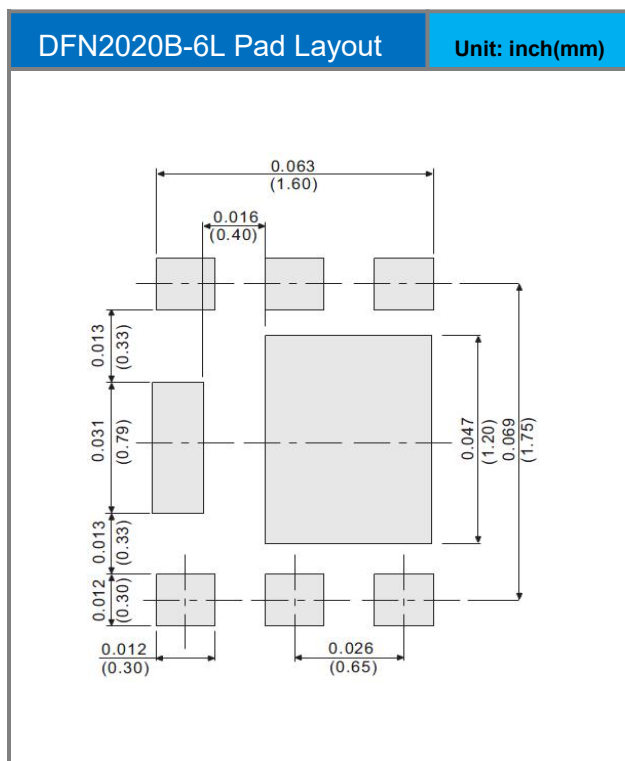
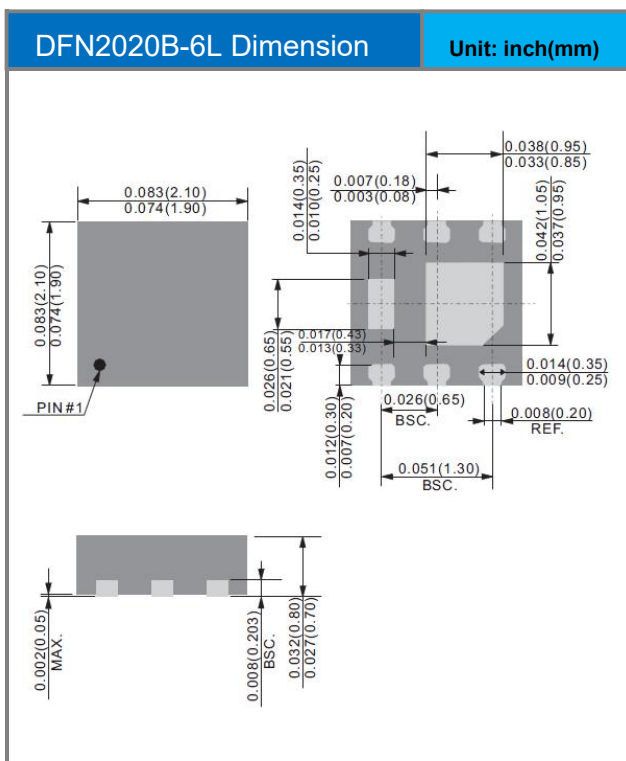
Fig.10 Capacitance vs. Drain-Source Voltage

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Part No Packing Code Version

Part No Packing Code	Package Type	Packing Type
CSM212N8D2-2	DFN2020B-6L	3K pcs / 7" reel

Packaging Information & Mounting Pad Layout



CSM212N8D2-2

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