

Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V _{DS}	-30	V
Gate-Source Voltage		V _{GS}	<u>+</u> 20	V
Continuous Drain Current	T _A =25°C		-15	
	T _A =70°C		-10	A
Pulsed Drain Current ^(Note 1)		I _{DM}	-48	А
Power Dissipation	T _A =25°C	_	1.7	
	T _A =70°C	PD PD	1.1	W
Operating Junction and Storage Temperature Range		T _J ,T _{STG}	-55~150	°C
Typical Thermal Resistance Junction to Ambient ^(Note 5)		R _{eja}	62.5	°C/W



Electrical Characteristics (T_A=25[°]C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV_{DSS}	V _{GS} =0V,I _D =-250uA	-30	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$, $I_D=-250$ uA	-1	-1.6	-2.5	V
Drain-Source On-State Resistance	$R_{\text{DS(on)}}$	V _{GS} =-10V,I _D =-15A	-	3	5	mΩ
Drain-Source On-State Resistance	$R_{\text{DS(on)}}$	V _{GS} =-4.5V,I _D =-10A	-	7	9	mΩ
Zero Gate Voltage Drain Current	I _{DSS}	V_{DS} =-30V, V_{GS} =0V	-	-	-1	uA
Gate-Source Leakage Current	I _{GSS}	V _{GS} = <u>+</u> 20V,V _{DS} =0V	-	-	<u>+</u> 100	nA
Dynamic (Note 6)						
Total Gate Charge	Qg	V _{DS} =-15V, I _D =-10A, V _{GS} =-4.5V ^(Note 1,2)	-	26	-	
Gate-Source Charge	Q_gs		-	8.7	-	nC
Gate-Drain Charge	Q_gd		-	8.6	-	
Input Capacitance	Ciss	V _{DS} =-15V, V _{GS} =0V, f=1.0MHZ	-	3168	-	
Output Capacitance	Coss		-	393	-	pF
Reverse Transfer Capacitance	Crss		-	258	-	
Turn-On Delay Time	td _(on)	V _{DS} =-15V,I _D =-1A, V _{GEN} =-10V, R _G =6Ω (Note 1,2)	-	11	-	
Turn-On Rise Time	tr		-	14	-	ns
Turn-Off Delay Time	td _(off)		-	102	-	
Turn-Off Fall Time	tf		-	47	-	
Drain-Source Diode						
Maximum Continuous Drain-Source	ls			-	-12	A
Diode Forward Current	IS		-			
Diode Forward Voltage	V_{SD}	I _S =-1A, V _{GS} =0V	-	-0.7	-1.0	V

NOTES :

- 1. Pulse width300us, Duty cycle2%
- 2. Essentially independent of operating temperature typical characteristics.
- 3. The maximum current rating is package limited.
- 4. Repetitive rating, pulse width limited by junction temperature T_{J(MAX)}=150°C. Ratings are based on low frequency and duty cycles to keep initial T_J =25°C.
- 5. $R_{\Theta JA}$ is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch² with 2oz.square pad of copper.
- 6. Guaranteed by design, not subject to production testing.



TYPICAL CHARACTERISTIC CURVES 20 V_{GS}=-10V,-8V, -5V,-4.5V V_{GS}=-3V -Ips-Drain-to-S ource Current(A) 16 12 8 V_{GS}=-2.5V 4 0 0 1 2 3 4 5 -V_{DS}- Drain-to-Source Voltage(V) **Fig.1 On-Region Characteristics** 12 R_{Ds}(on)- On-Resistance(mΩ) V_{GS}=-4.5V 10.5 9

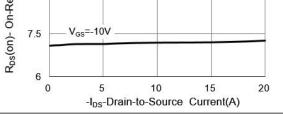
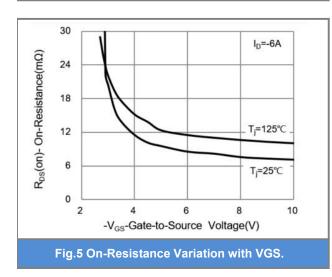


Fig.3 On-Resistance vs. Drain Current



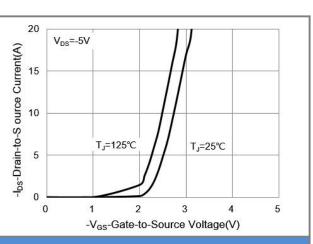


Fig.2 Transfer Characteristics

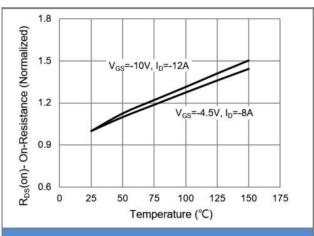
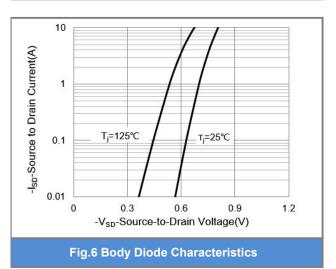


Fig.4 On-Resistance vs. Junction temperature





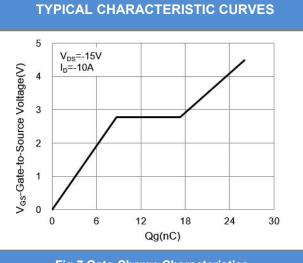


Fig.7 Gate-Charge Characteristics

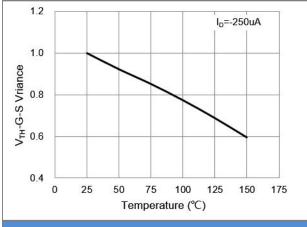
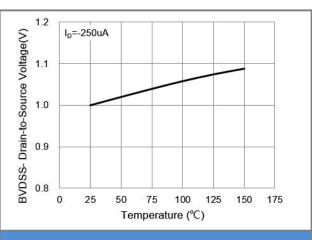


Fig.9 Threshold Voltage Variation with Temperature





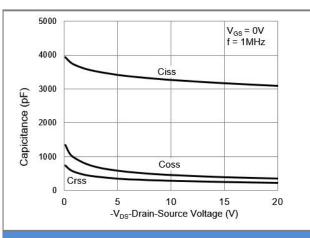


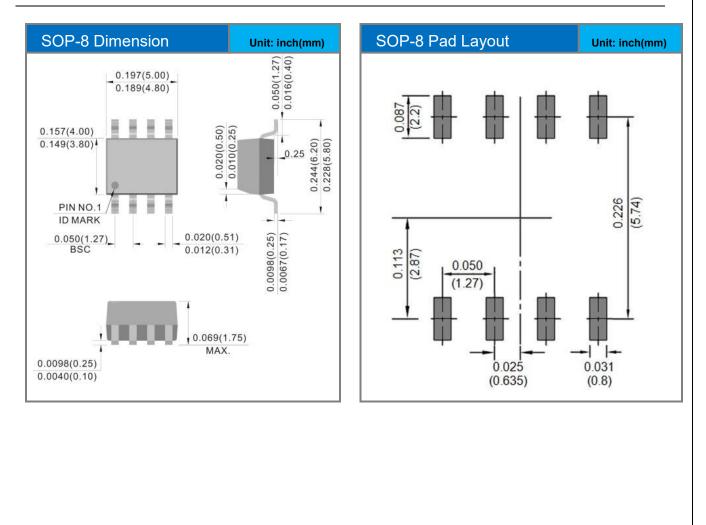
Fig.10 Capacitance vs. Drain-Source Voltage



Part No Packing Code Version

Part No Packing Code	Package Type	Packing Type		
CSM4409SOP8	SOP-8	3K / reel		

Packaging Information & Mounting Pad Layout





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