

20V P-Channel Enhancement Mode MOSFET

Voltage -20 V Current -4.0A

Features

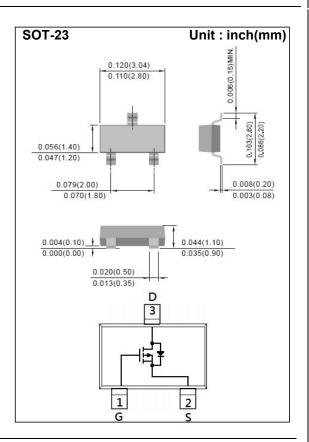
- RDS(ON), VGS@-4.5V, ID@-4.0A<55mΩ
- RDS(ON), VGS@-2.5V, ID@-2.8A<70mΩ
- RDS(ON), VGS@-1.8V, ID@-2.1A<95mΩ
- Advanced Trench Process Technology
- Specially Designed for Switch Load, PWM Application, etc

Mechanical Data

• Case: SOT-23 Package

• Terminals: Solderable per MIL-STD-750, Method 2026

• Approx. Weight: 0.0003 ounces, 0.0084 grams



Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V _{DS}	-20	V
Gate-Source Voltage		V _{GS}	<u>+</u> 12	V
Continuous Drain Current		I _D	-4.0	Α
Pulsed Drain Current		I _{DM}	-16	Α
Power Dissipation	T _a =25°C	Б	1.25	W
	Derate above 25°C	P _D	10	mW/°C
Operating Junction and Storage Temperature Range		T_{J}, T_{STG}	-55~150	°C
Typical Thermal resistance Junction to Ambient (Note 3)		$R_{\theta JA}$	100	°C/W



Electrical Characteristics (T_A=25 °C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS	
Static							
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =-250uA	-20	-	-	٧	
Gate Threshold Voltage	$V_{GS(th)}$	V _{DS} =V _{GS} , I _D =-250uA	-0.4	-0.62	-1.2	٧	
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =-4.5V, I _D =-4.0A	-	50	55	mΩ	
		V _{GS} =-2.5V, I _D =-2.8A	-	59	70		
		V _{GS} =-1.8V, I _D =-2.1A	-	74	95		
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-20V, V _{GS} =0V	-	-0.01	-1	uA	
Gate-Source Leakage Current	I _{GSS}	V _{GS} = <u>+</u> 12V, V _{DS} =0V	-	<u>+</u> 10	<u>+</u> 100	nA	
Dynamic							
Total Gate Charge	Qg		-	18	-	nC	
Gate-Source Charge	Q_{gs}	V_{DS} =-10V, I_{D} =-4.0A, V_{GS} =-4.5V (Note 1,2)	-	2	-		
Gate-Drain Charge	Q_{gd}		-	7	-		
Input Capacitance	Ciss	V _{DS} =-10V, V _{GS} =0V, f=1.0MHZ	-	756	-	pF	
Output Capacitance	Coss		-	75	-		
Reverse Transfer Capacitance	Crss		-	58	-		
Switching							
Turn-On Delay Time	td _(on)	V _{DD} =-10V, I _D =-4.0A,	-	5	-		
Turn-On Rise Time	tr	V_{DD} 10V, 1D4.0A, V_{GS} =-4.5V, R_{G} =6 Ω (Note 1,2)	-	61	-	ns	
Turn-Off Delay Time	td _(off)		-	70	-		
Turn-Off Fall Time	tf		-	137	ı		
Drain-Source Diode							
Maximum Continuous Drain-Source	l-				1 5	^	
Diode Forward Current	Is		-	-	-1.5	Α	
Diode Forward Voltage	V _{SD}	I _S =-1.0A, V _{GS} =0V	-	0.76	-1.2	٧	

NOTES:

- 1. Pulse width < 300us, Duty cycle < 2%
- 2. Essentially independent of operating temperature typical characteristics.
- 3. Rejah is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper
- 4. The maximum current rating is package limited



TYPICAL CHARACTERISTIC CURVES

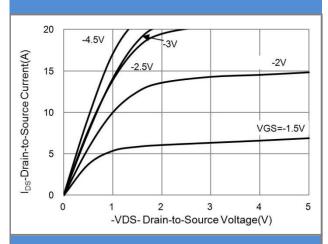


Fig.1 On-Region Characteristics

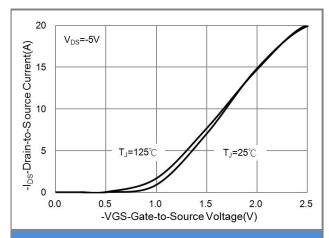


Fig.2 Transfer Characteristics

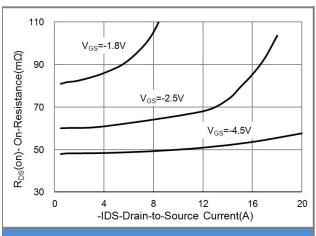


Fig.3 On-Resistance vs. Drain Current

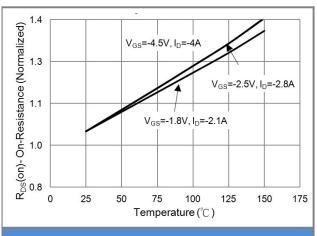
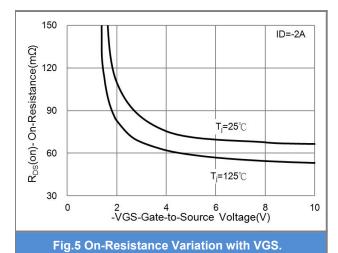


Fig.4 On-Resistance vs. Junction temperature



-I_S-Source to-Drain Current(A) 0.01 0.3 0.6 -VSD-Source-to-Drain Voltage(V)

T_j=125℃

Fig.6 Body Diode Characteristics

T_i=25°C

1.2

10



TYPICAL CHARACTERISTIC CURVES

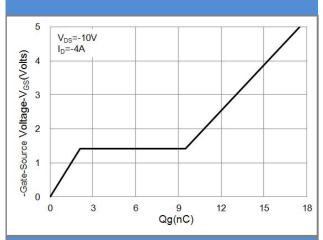


Fig.7 Gate-Charge Characteristics

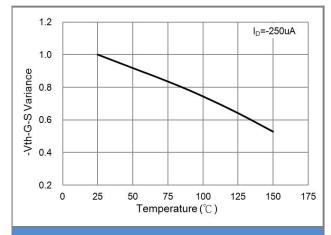


Fig.8 Threshold Voltage Variation with Temperature.

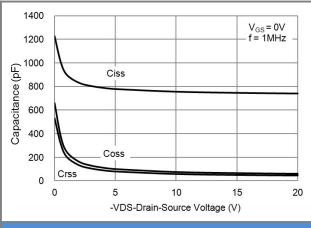


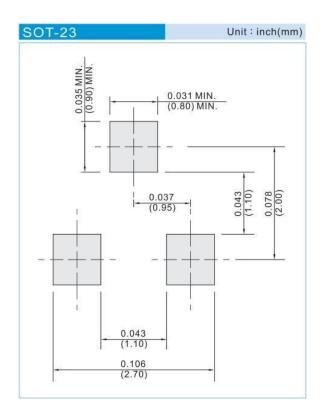
Fig.9 Capacitance vs. Drain-Source Voltage.



PART NO PACKING CODE VERSION

Part No Packing Code	Package Type	Packing type
CSM2305S23	SOT-23	3K pcs / 7" reel

MOUNTING PAD LAYOUT





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