

12V P-Channel Enhancement Mode MOSFET

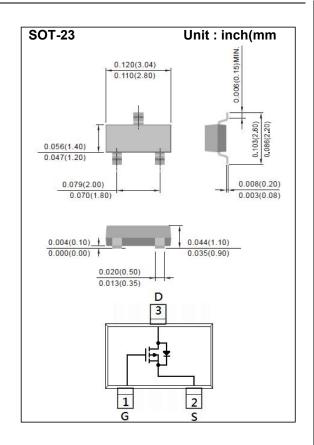
Voltage -12 V Current -6A

Features

- RDS(ON), VGS@-4.5V, ID@-6.0A<30mΩ
- RDS(ON), VGS@-2.5V, ID@-5.0A<39mΩ
- RDS(ON), VGS@-1.8V, ID@-2.5A<48mΩ
- Advanced Trench Process Technology
- High density cell design for ultra low on-resistance

Mechanical Data

- Case: SOT-23 Package
- Terminals: Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 0.0003 ounces, 0.0084 grams



Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNITS	
Drain-Source Voltage		V _{DS}	-12	V
Gate-Source Voltage		V_{GS}	<u>+</u> 12	V
Continuous Drain Current		I _D	-6	Α
Pulsed Drain Current		I _{DM}	-24	Α
Power Dissipation	T _a =25°C		2.8	W
	Derate above 25°C	P _D	22	mW/ °C
Operating Junction and Storage Tem	T_{J}, T_{STG}	-55~150	°C	
Typical Thermal Resistance Junction to Ambient, t<10s (Note 3)		$R_{\theta JA}$	44.6	°C/W



Electrical Characteristics (T_A=25 °C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS	
Static							
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V,I _D =-250uA	-20	-	-	V	
Gate Threshold Voltage	$V_{GS(th)}$	V _{DS} =V _{GS} , I _D =-250uA	-0.35	-0.6	-0.9	V	
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =-4.5V,I _D =-6.0A	-	25	30	mΩ	
		V _{GS} =-2.5V,I _D =-5.0A	-	30	39		
		V _{GS} =-1.8V,I _D =-2.5A	-	35	48		
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-12V,V _{GS} =0V	-	-0.01	-1.0	uA	
Gate-Source Leakage Current	I _{GSS}	V _{GS} = <u>+</u> 12V,V _{DS} =0V	-	<u>+</u> 10	<u>+</u> 100	nA	
Dynamic (Note 6)							
Total Gate Charge	Qg	101/ 1 704	-	18.9	-	nC	
Gate-Source Charge	Q_{gs}	V_{DS} =-10V, I_{D} =-7.2A, V_{GS} =-4.5V (Note 1,2)	-	2.8	-		
Gate-Drain Charge	Q_{gd}		-	4.2	-		
Input Capacitance	Ciss	V _{DS} =-10V, V _{GS} =0V, f=1.0MHZ	-	1785	-	pF	
Output Capacitance	Coss		-	152	-		
Reverse Transfer Capacitance	Crss		-	125	-		
Turn-On Delay Time	td _(on)	V_{DS} =-10V, I_{D} =-7.2A, V_{GEN} =-4.5V, R_{L} =10 Ω	-	12	-		
Turn-On Rise Time	tr		-	68	-	ns	
Turn-Off Delay Time	td _(off)		-	82	-		
Turn-Off Fall Time	tf	R _G =012 \ / /	-	35	-		
Drain-Source Diode							
Maximum Continuous Drain-Source					4.5		
Diode Forward Current	Is		-	-	-1.5	A	
Diode Forward Voltage	V _{SD}	I _S =-1A, V _{GS} =0V	-	-0.64	-1.2	V	

NOTES:

- 1. Pulse width<a>300us, Duty cycle<a>2%
- 2. Essentially independent of operating temperature typical characteristics.
- 3. The maximum current rating is package limited.
- 4. Repetitive rating, pulse width limited by junction temperature TJ(MAX)=150°C. Ratings are based on low frequency and duty cycles to keep initial TJ =25°C.
- 5. R_{OJA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch² with 2oz.square pad of copper.
- 6. Guaranteed by design, not subject to production testing.



TYPICAL CHARACTERISTIC CURVES

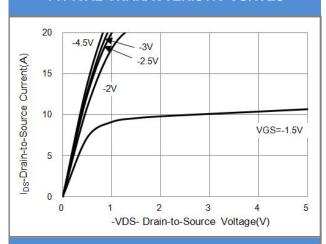


Fig.1 On-Region Characteristics

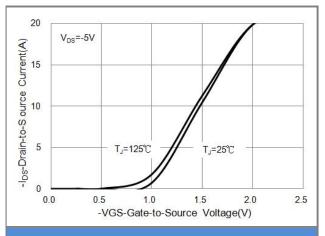


Fig.2 Transfer Characteristics

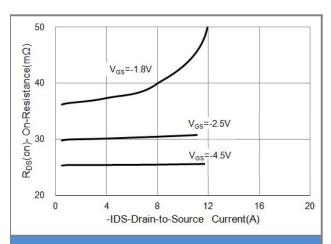


Fig.3 On-Resistance vs. Drain Current

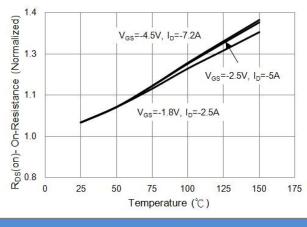


Fig.4 On-Resistance vs. Junction temperature

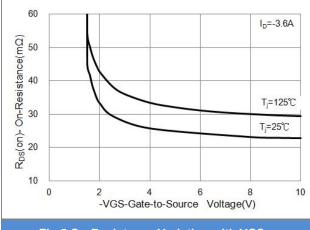


Fig.5 On-Resistance Variation with VGS.

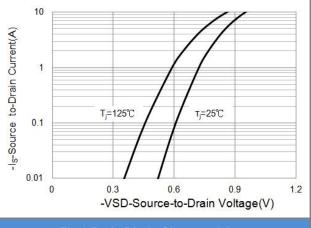


Fig.6 Body Dlode CharacterIsIcs



TYPICAL CHARACTERISTIC CURVES

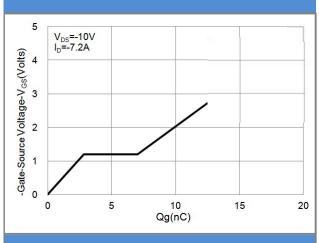


Fig.7 Gate-Charge Characteristics

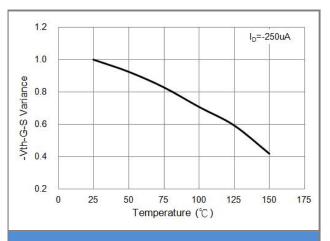


Fig.8 Threshold Voltage Variation with Temperature.

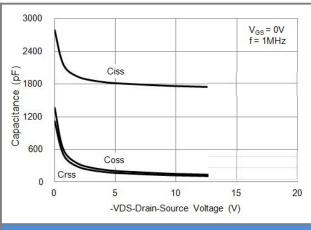


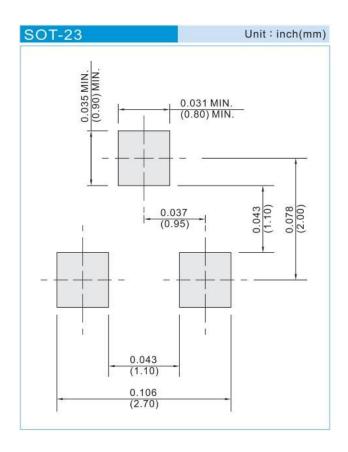
Fig.9 Capacitance vs. Drain-Source Voltage.



PART NO PACKING CODE VERSION

Part No Packing Code	Package Type	Packing Type
CSM2333S23	SOT-23	3K pcs / 7" reel

MOUNTING PAD LAYOUT





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