

• Approx. Weight: 0.0029 ounces, 0.083 grams

Maximum Ratings and Thermal Characteristics (T_A=25[°]C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS	
Drain-Source Voltage		V _{DS}	-30		
Gate-Source Voltage		V _{GS}	<u>+</u> 20	- V	
Continuous Drain Current	T _A =25°C		-5		
	T _A =70°C		-3	A	
Pulsed Drain Current ^(Note 1)		I _{DM}	-20		
Power Dissipation	T _A =25°C	_	1.7	W	
	T _A =70°C	PD PD	1.1		
Operating Junction and Storage Temperature Range		T_{J},T_{STG}	-55~150	°C	
Typical Thermal Resistance Junction to Ambient ^(Note 5)		R _{0JA}	73.5	°C/W	



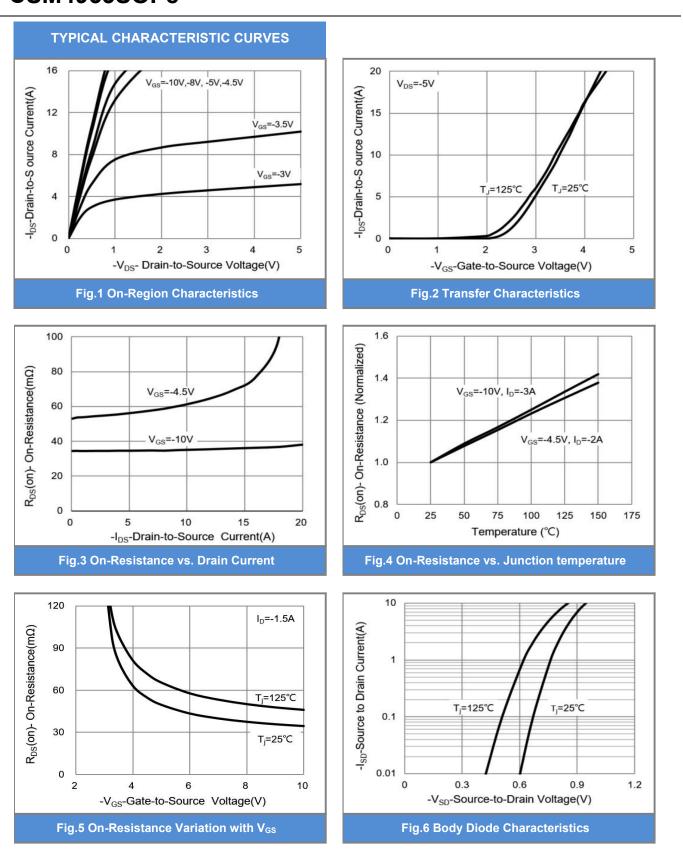
Electrical Characteristics (T_A=25[°]C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =-250uA	-30	-	-	
Gate Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS}$, $I_{D}=-250$ uA	-1	-1.6	-2.5	
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =-10V, I _D =-3A	-	42	52	mΩ
		V _{GS} =-4.5V, I _D =-2A	-	62	70	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-30V, V _{GS} =0V	-	-	-1	uA
Gate-Source Leakage Current	I _{GSS}	V _{GS} = <u>+</u> 20V, V _{DS} =0V	-	-	<u>+</u> 100	nA
Dynamic (Note 6)						
Total Gate Charge	Qg	V_{DS} =-15V, I _D =-3A, V _{GS} =-4.5V ^(Note 1,2)	-	4.8	-	
Gate-Source Charge	Q _{gs}		-	1.7	-	nC
Gate-Drain Charge	Q _{gd}		-	1.7	-	
Input Capacitance	Ciss	V _{DS} =-15V, V _{GS} =0V, f=1.0MHZ	-	516	-	pF
Output Capacitance	Coss		-	83	-	
Reverse Transfer Capacitance	Crss		-	61	-	
Turn-On Delay Time	td _(on)	V _{DS} =-15V, I _D =-1A, V _{GEN} =-10V, R _G =6Ω (Note 1,2)	-	5.6	-	
Turn-On Rise Time	tr		-	8.5	-	ns
Turn-Off Delay Time	td _(off)		-	27	-	
Turn-Off Fall Time	tf		-	18	-	
Drain-Source Diode						
Maximum Continuous Drain-Source	ls			-	-4	A
Diode Forward Current	IS		-			
Diode Forward Voltage	V _{SD}	I _S =-1A, V _{GS} =0V	-	-0.75	-1	V

NOTES :

- 1. Pulse width <300us, Duty cycle <2%
- 2. Essentially independent of operating temperature typical characteristics.
- 3. The maximum current rating is package limited.
- 4. Repetitive rating, pulse width limited by junction temperature T_{J(MAX)}=150°C. Ratings are based on low frequency and duty cycles to keep initial T_J =25°C.
- 5. R_{0JA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch² with 2oz.square pad of copper.
- 6. Guaranteed by design, not subject to production testing.







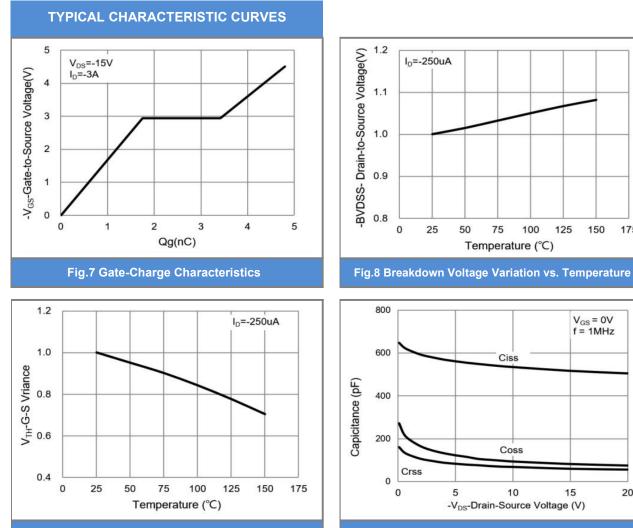


Fig.9 Threshold Voltage Variation with Temperature

Fig.10 Capacitance vs. Drain-Source Voltage

75

Temperature (°C)

Ciss

Coss

10

100

125

150

V_{GS} = 0V f = 1MHz

15

175

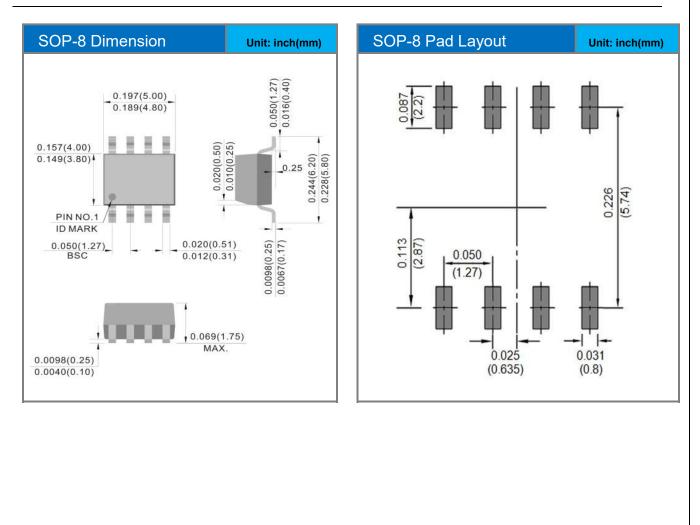
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Part No Packing Code Version

Part No Packing Code	Package Type	Packing Type		
CSM4953SOP8	SOP-8	3K / reel		

Packaging Information & Mounting Pad Layout





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