

CSM3134DFN1006-3

20V N-Channel Enhancement Mode MOSFET

Voltage 20 V Current 2 A

Features

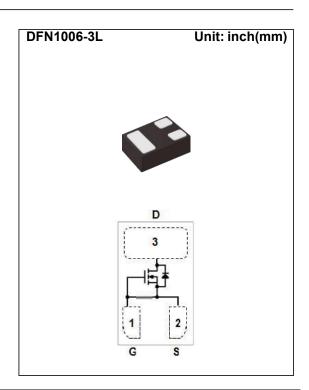
- Advanced Trench Process Technology
- Specially Designed for Switch Load, PWM Application, etc

Mechanical Data

• Case: DFN1006-3L Package

• Terminals : Solderable per MIL-STD-750, Method 2026

• Approx. Weight: 0.00002 ounces, 0.0007 grams



Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNITS		
Drain-Source Voltage		V _{DS}	20	V	
Gate-Source Voltage		V _{GS}	<u>+</u> 12		
Continuous Drain Current (Note 4)		I _D	2	_ A	
Pulsed Drain Current (Note 1)		I _{DM}	3		
Power Dissipation	T _a =25°C	1	500	mW	
	Derate above 25°C	P _D	4	mW/°C	
Operating Junction and Storage Temperature Range		TJ,Tstg	-55~150	°C	
Typical Thermal resistance					
Junction to Ambient (Note 3,4)		R _{0JA}	250	°C/W	

Limited only By Maximum Junction Temperature



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Electrical Characteristics (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =250uA	20	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	V _{DS} =V _{GS} , I _D =250uA	0.3	0.5	1	
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =4.5V, I _D =500mA	-	220	280	mΩ
		V _{GS} =2.5V, I _D =400mA	-	250	400	
		V _{GS} =1.8V, I _D =200mA	-	300	550	
		V _{GS} =1.5V, I _D =100mA	-	340	800	
		V _{GS} =1.2V, I _D =100mA	-	480	1500	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =20V, V _{GS} =0V	-	-	1	uA
Gate-Source Leakage Current	I _{GSS}	V _{GS} = <u>+</u> 12V, V _{DS} =0V	-	-	<u>+</u> 10	
Dynamic (Note 5)						
Total Gate Charge	Qg	V _{DS} =10V, I _D =500mA, V _{GS} =4.5V ^(Note 2)	-	1.1	-	nC
Gate-Source Charge	Q_gs		-	0.16	-	
Gate-Drain Charge	Q_gd		-	0.12	-	
Input Capacitance	Ciss	V _{DS} =10V, V _{GS} =0V, f=1MHZ	-	46	-	
Output Capacitance	Coss		-	15	-	pF
Reverse Transfer Capacitance	Crss		-	3	-	
Turn-On Delay Time	td _(on)	$\begin{array}{c} V_{DD}\text{=}10\text{V},\ I_{D}\text{=}500\text{mA},\\ V_{GS}\text{=}4.5\text{V},\\ R_{G}\text{=}6\Omega\ ^{\text{(Note 2)}} \end{array}$	-	5.3	-	
Turn-On Rise Time	tr		-	22	-	
Turn-Off Delay Time	td _(off)		-	43	-	ns
Turn-Off Fall Time	tf		-	31	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	Is		-	-	500	mA
Diode Forward Voltage	V_{SD}	I _S =500mA, V _{GS} =0V	-	0.7	1	V

NOTES:

- 1. Pulse width < 300us, Duty cycle < 2%.
- 2. Essentially independent of operating temperature typical characteristics.
- 3. R_{OJA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper.
- 4. The maximum current rating is package limited.
- 5. Guaranteed by design, not subject to production testing.



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TYPICAL CHARACTERISTIC CURVES

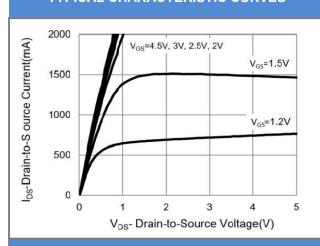


Fig.1 On-Region Characteristics

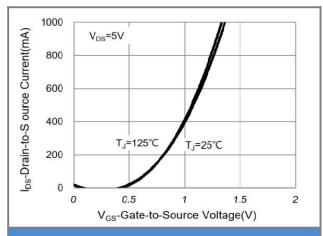


Fig.2 Transfer Characteristics

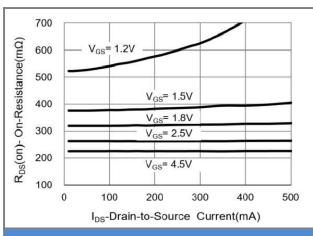


Fig.3 On-Resistance vs. Drain Current

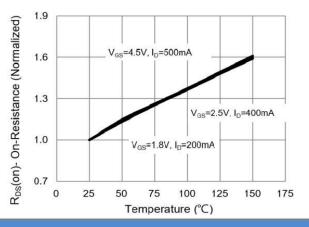


Fig.4 On-Resistance vs. Junction temperature

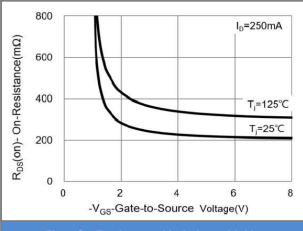


Fig.5 On-Resistance Variation with V_{GS}

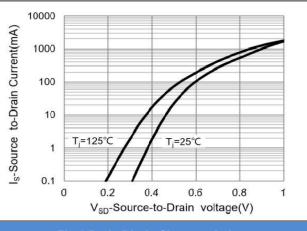


Fig.6 Body Diode Characteristics



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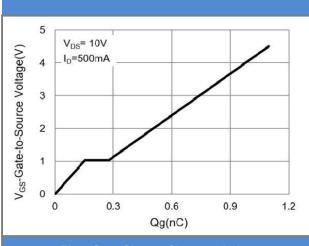


Fig.7 Gate-Charge Characteristics

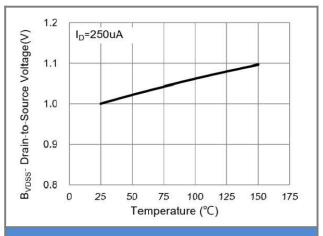


Fig.8 Breakdown Voltage Variation vs. Temperature

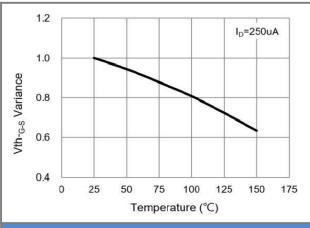


Fig.9 Threshold Voltage Variation with Temperature

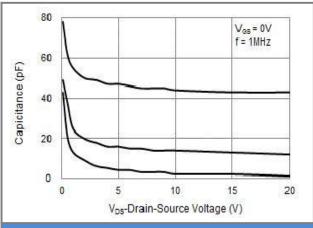


Fig.10 Capacitance vs. Drain-Source Voltage

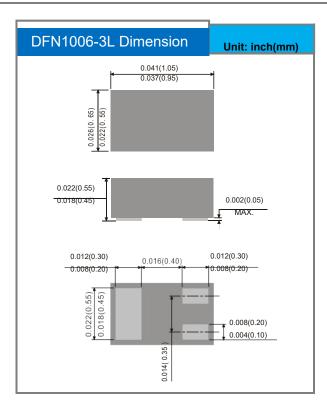


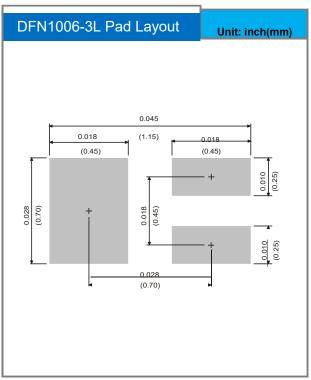
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Part No Packing Code Version

Part No Packing Code	Package Type	Packing Type		
CSM3134DF1006-3	DFN1006-3L	10K pcs / 7" reel		

Packaging Information & Mounting Pad Layout







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