

CSM620N03S23

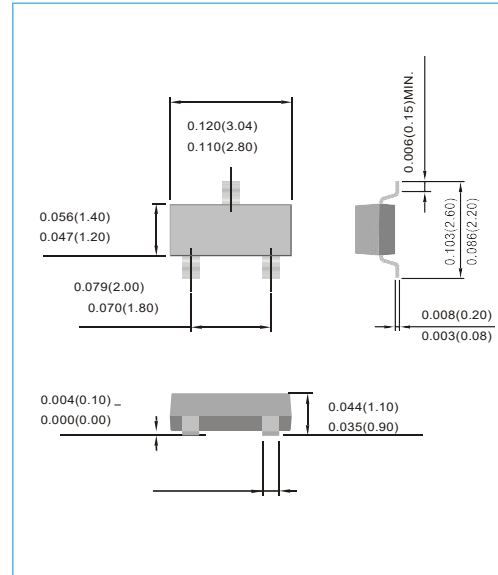
60V N-Channel Enhancement Mode MOSFET

FEATERES

- $R_{DS(ON)}$, $V_{GS}@10V, I_{DS}@500mA=1.5 \Omega$
- $R_{DS(ON)}$, $V_{GS}@4.5V, I_{DS}@200mA=2 \Omega$
- $R_{DS(ON)}$, $V_{GS}@2.5V, I_{DS}@100mA=3 \Omega$
- Advanced Trench Process Technology
- High Density Cell Design For Ultra Low On-Resistance
- Very Low Leakage Current In Off Condition
- Specially Designed for Battery Operated Systems, Solid-State Relays Drivers : Relays, Displays, Lamps, Solenoids, Memories, etc.

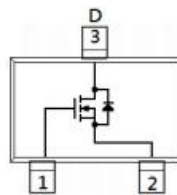
SOT-23

Unit : inch(mm)



MECHANICAL DATA

- Case: SOT-23 Package
- Terminals: Solderable per MIL-STD-750, Method 2026
- Apporx. Weight: 0.0003 ounce, 0.0084 gram



Maximum Ratings and Thermal Characteristics (T_A =25°C unless otherwise noted)

PA RA ME TE R	S ymbol	Li mi t	Uni ts
Drain- Source Voltage	V_{DS}	60	V
Gate -Source Voltage	V_{GS}	± 20	V
Continuous Drain Current	I_D	300	m A
Pulsed Drain Current ¹⁾	I_{DM}	2000	m A
Maximum Power Dissipation $T_A=25^\circ C$ $T_A=75^\circ C$	P_D	350 210	M W
Operating Junction and Storage Temperature Rang e	T_J, T_{STG}	- 55 to + 150	°C
Junction-to Ambient Thermal Resistance(PCB mounted) ²	$R_{\theta JA}$	357	°C/ W

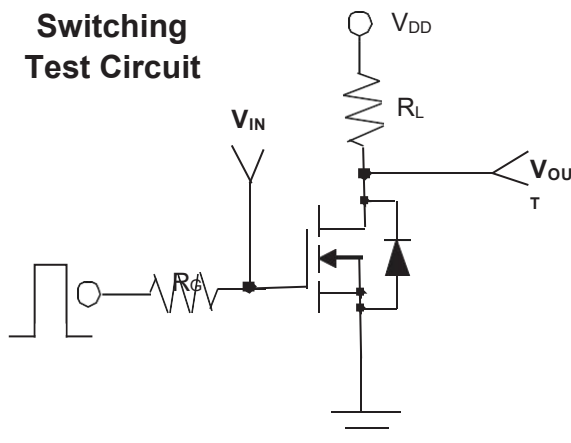
Note: 1. Maximum DC current limiNote: 1. Maximum DC current limited by the package
 2. Surface mounted on FR4 board, t < 5 sec

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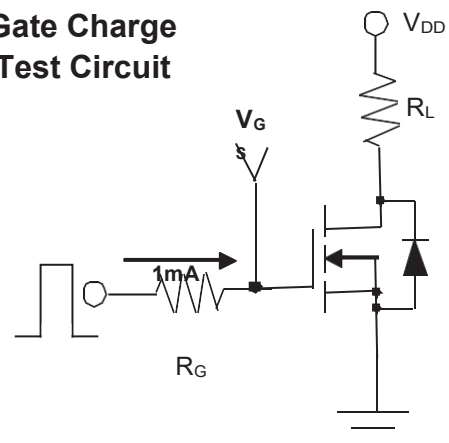
ELECTRICAL CHARACTERISTICS

Parameter	Symbol	Test Condi t i o n	Mi n.	Typ .	Max.	Uni ts
Static						
Drain-Source Breakdown Voltage	$BV_{DS\ S}$	$V_{GS}=0V\ ,\ I_D=10\ \mu A$	50	-	-	V
Gate Threshold Voltage	$V_{GS\ (th)}$	$V_{DS}=V_{GS}\ ,\ I_D=250\ \mu A$	0.8	-	1.5	V
Drain- Source On- State Resistance	$R_{DS\ (on)}$	$V_{GS}=2.5V\ ,\ I_D=100mA$	-	2.8	3	Ω
D rain- Source On- StateResi stance	$R_{DS\ (on)}$	$V_{GS}=4.5V\ ,\ I_D=200mA$	-	1.8	2	
Drain- Source O n- State Resistance	$R_{DS\ (on)}$	$V_{GS}=10V\ ,\ I_D=500mA$	-	1.6	1.5	
Zero Gate Voltage Drain Current	$I_{DS\ S}$	$V_{DS}=50V\ ,\ V_{GS}=0V$	-	-	1	μA
Gate Body Leakage	$I_{GS\ S}$	$V_{GS}=\pm 20V\ ,\ V_{DS}=0\ V$	-	-	± 10	μA
Forward Transconductance	g_{fs}	$V_{DS}=10V\ ,\ I_D=250m\ A$	100	-	-	m S
Dynamic						
Total Gate Charge	Q_g	$V_{DS}=25\ V,\ I_D=250m\ A$ $V_{GS}=4.5V$	-	-	1.0	nC
Turn-On Ti me	t_{on}	$V_{DD}=30V\ ,\ R_L=100\ \wedge$ $I_D=300mA\ ,\ V_{GEN}=10V$ $R_G=6\ \wedge$	-	-	40	ns
Turn-Off Ti me	t_{off}		-	-	150	
Input Capacitance	C_{iss}	$V_{DS}=25V\ ,\ V_{GS}=0\ V$ $f=1.0\ MH_z$	-	-	50	pF
Output Capacitance	C_{oss}		-	-	10	
Reverse Transfer Capacitance	C_{rss}		-	-	5	
Source- Drain D i ode						
D i ode Forward Voltage	V_{SD}	$I_s=250mA\ ,\ V_{GS}=0V$	-	0.82	1.2	V
Continuous D i ode Forward Current	I_s	-	-	-	300	m A
Pulse D i ode Forward Current	I_{SM}	-	-	-	2000	m A

Switching Test Circuit

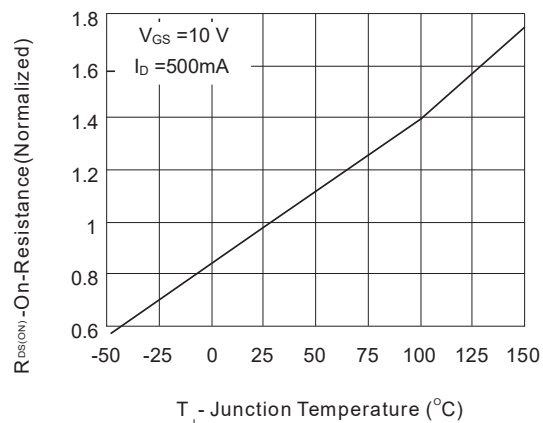
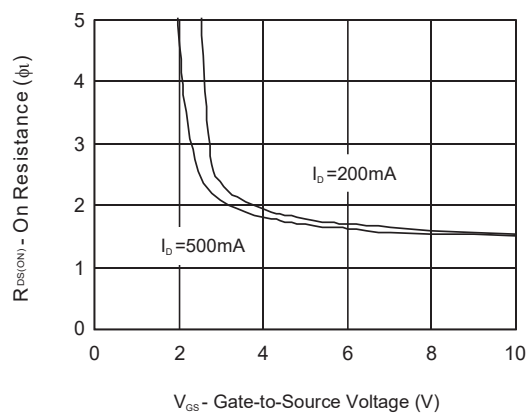
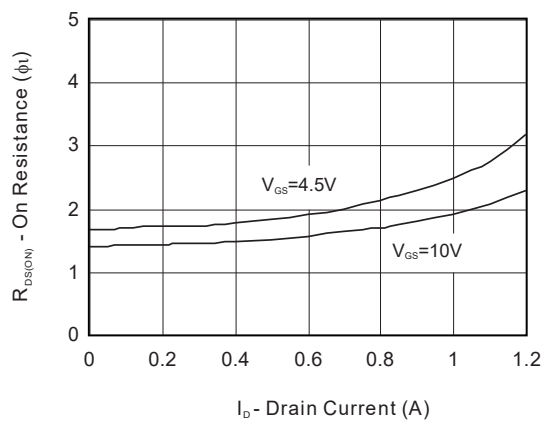
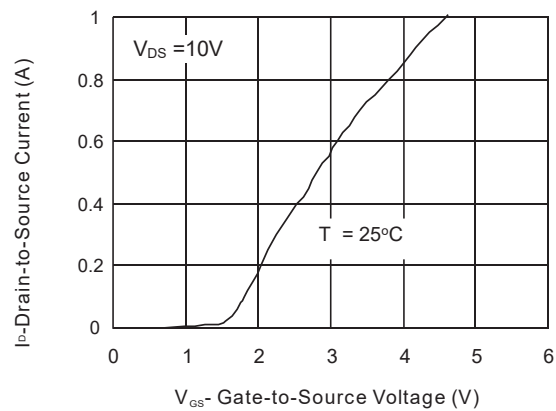
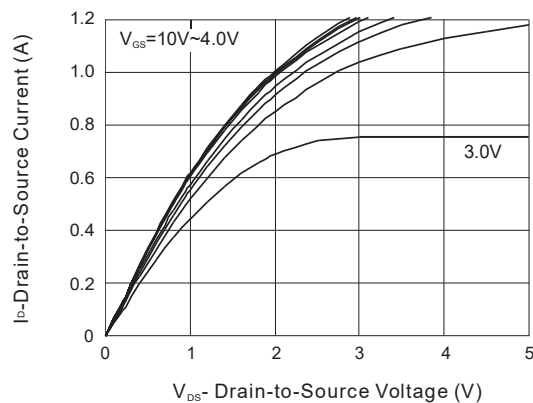


Gate Charge Test Circuit

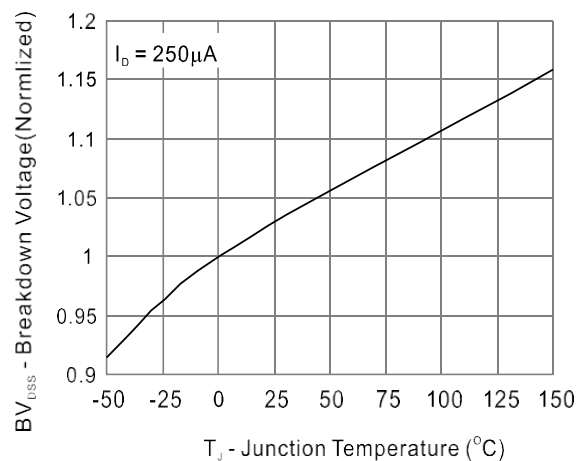
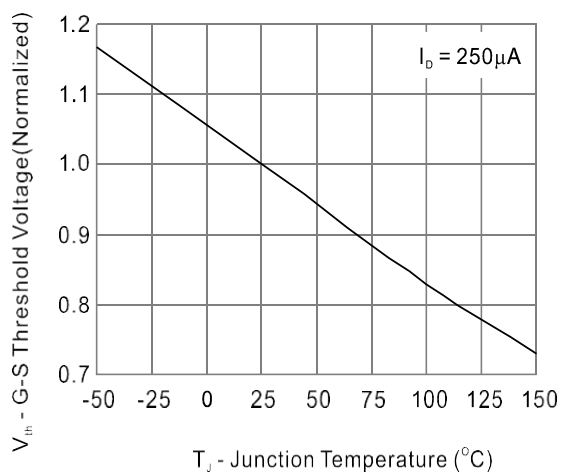
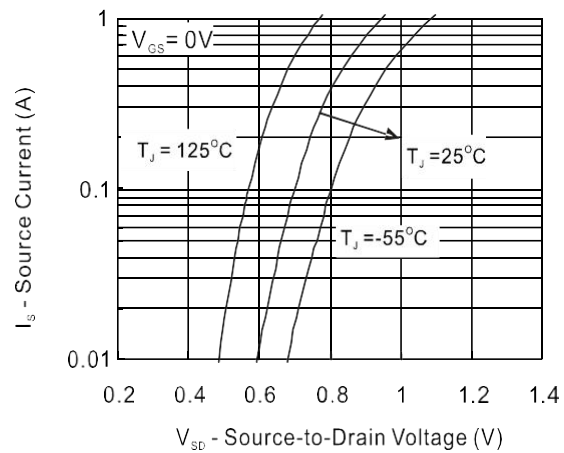
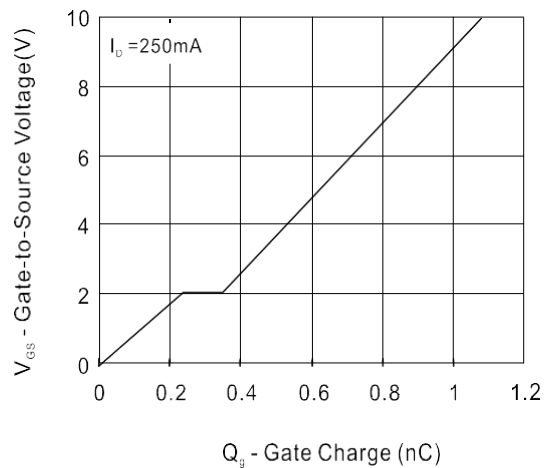


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Typical Characteristics Curves ($T_A = 25^\circ\text{C}$, unless otherwise noted)



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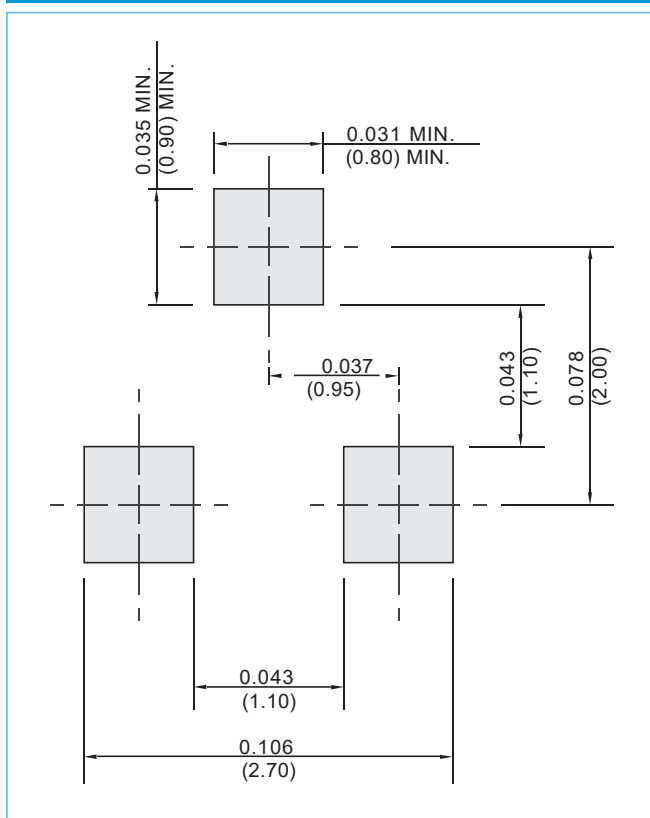


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MOUNTING PAD LAYOUT

SOT-23

Unit: inch(mm)



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